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We return to earlier projects.

YOUR Sinclair Projects has changed this month. To reflect the interest and needs of readers, we have expanded coverage of matters in the Sinclair hardware market and made improvements to the traditional areas of the magazine.

Our aim is to make Sinclair Projects the complete hardware companion for your Sinclair machine — essential reading for those who want to know how commercial hardware works and how you can build something yourself.

To achieve it we will be reviewing in depth all the latest peripherals on the Sinclair market, showing what they do, of what they comprise, and how they perform their tasks. This month our chief reviewer, John Lambert, looks inside the Dean Electronics thermal printer and the Fox Electronics programmable joystick for the Spectrum, among many other items.

We also intend to provide lengthy reviews of important products or comparisons of groups — of peripherals which perform similar tasks. In this issue Mike Wright considers the possibilities of the new QL, the Sinclair Quantum Leap.

The world of computers can often seem an inhuman place but without people there would be no machines, no programs, no users. From now we will be promoting the human aspect by meeting some of the people behind the latest machines or developments.

Everyone is talking about the QL at the moment, so Nigel Clark went to speak to one of the men behind its development. Though still a young man, David Karlin has already done a great deal in the world of micro-electronics.

The number of build-it-yourself projects has been reduced from six to four but they are still as interesting as in previous issues. With the growing interest in robots, as shown by the number of readers who have enquired about the Prowler, we have a project to build a simple arm. We have made the device as uncomplicated as possible, so that we give the essentials of what is involved while still making something useful.

In a project for the Spectrum, Richard Sargent shows how to build a one-chip Centronics interface. That is the industry standard interface for printers and should interest anyone wanting to improve hard copies of information.

The other two articles are software projects. Both have been chosen because of their help to serious users of the Spectrum. One permits people to build a flexible filing system which can be used to accommodate a variety of records. The other shows how accurate mathematics can be performed.

Managing editor Nigel Clark Consultant editor David Buckley Managing production editor Harold Mayes MBE News writer John Lambert Design Elaine Bishop Advertisement manager John Ross Advertisement Executive Robert Marcus Editorial assistant Colette McDermott Production assistant Dezi Epaminondou Managing director Terry Cartwright Chairman Richard Hease.

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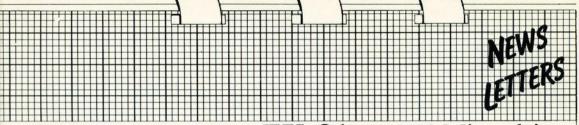
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WH SMITH



Power from the ZX-81

DESPITE having large amounts of computing power at their disposal, many large companies are using the ZX-81 to help them with their research. They have discovered that the little machine, with the addition of the Forth ROM cartridge from Skywave Software, gives them many

OL orders rush

SIR CLIVE SINCLAIR'S reputation for launching good products is such that orders for the QL have been flooding in. The only machines to have been seen so far are those on display at the launch yet there is now a waiting list "well into the thousands".

Although the first machines were expected to be delivered early in March, the unexpected rush means that a backlog will develop.

There were more than 400 orders in the first two days after the launch and now they are arriving at 500 a day. facilities at a low cost — the ROM costs only £25.

David Husband of Skywave, based at Boscombe, Bournemouth, says he was surprised at first when he started receiving orders from large companies such as ICI, universities and a number of Government establishments. "It does not surprise me any more; they just realised what can be done with the ZX-81 and the ROM," he says.

The ROM with its multi-tasking capability makes the ZX-81 a useful controller of applications, particularly in laboratories and as a teaching aid. Husband says it would be ideal for a project such as the weather station being built in *Sinclair Projects*, where the measurements of temperature and pressure could be performed at regular intervals but the user could still program the machine. It is also possible to have a number of windows on the screen in the same way as the new QL.

ZX-Microdrive design frozen

THE DESIGN of the Spectrum Microdrive has been frozen and there are no plans to make the improvements which have been made for the QL Microdrive. A spokesman for Sinclair Research said most of the changes had been cosmetic but admitted that they allowed an additional 15K of storage to be guaranteed for the QL version.

The ZX Microdrive sold for the Spectrum has a minimum of 85K of storage, whereas the QL Microdrive could supply 100K.

David Karlin, one of the leading members of the QL design team, said that a number of engineering changes had been made and there had been a great deal more error-checking and attempts to increase the performance of the controller chip.

A spokesman added that the changes would be difficult to implement on the ZX Microdrive.

Plea for rampack

AS A fairly new owner of a ZX-81, I am still using the unexpanded 1K version. Now, however, I wish to expand to the much more useful 16K package. Nowhere can I find a circuit diagram for an add-on RAM pack. Can you advise me of anyone who can supply a RAM pack in kit form? The object, obviously, is to do the job as cheaply as possible.

R J F Richardson, Harrogate.

• A 16K RAM pack is a complicated circuit and since they can be obtained ready-built from as little as about £15 it is not worth considering trying to make one. The parts alone would probably cost as much.

More support call

HAVING bought the October/November issue for the purpose of constructing the Battery-Backed RAM, the first three-quarters of a page were completely over my head. I was therefore very disappointed to find that not only was I unable to understand the article but the RAM board was of a rather strange capacity i.e., 8KB.

When programming my ZX-81, using the 16K programs freely available in the computer magazines, I frequently experience problems with supply voltage fluctuations and computer screen lock-up. The only way to restore computer functions is to re-set the mains supply, therefore losing the program, if not already lost.

I understand that the problems are usually found with the ZX-81 but you can see that this problem requires a 16K battery-backed RAM. Is it possible to modify the 8K RAM project to function as a 16K board by using more 2K RAMs or different components?

K J Bryan, Deal, Kent.

• You misunderstand the purpose of the battery-backed RAM. The project was to allow one to store machine code routines rather than Basic programs and is mapped outside the Basic program area. To overcome your problem, you could build the battery-back-up system of the December/January issue.

Teleprinter printer

LOOKING through Sinclair Projects and considering building the Radio Teleprinter, I decided to write a short machine code subroutine to simulate the output from the interface. It showed two problems with page 17, the machine program which I have not noticed in Update:

Line 16514 Data: missing a 00. Dec. address 16523 should read 16528.

At 16600 the call address given may be satisfactory for old ROM users but it caused me a few headaches. I think that new ROM users should have:

16600 40D8 CD 2B 0F CALL SLOW.

> Malcolm Purves, Bristol.



Cheap connector for the Spectrum

THE NEW I/O port from Multitron gives the Spectrum a means of communicating with the outside world. Using the port it could control motors, turn lights on and off, or detect when a switch has been closed. What it does is to transfer signals to and from the outside in a form the CPU can understand.

The port is an uncased PCB with through connector based on the Intel 8255 AP-5 chip. The chip has three 8-bit ports — A, B and C — and a control register D, the addresses being 31, 63, 95 and 127 respectively. Each port can be set to either input or output, with the upper and lower nibbles of port C capable of being set independently to either.

Two more modes of operation are available which allow strobed I/O with handshaking and strobed bi-directional operation; in both cases the data can be latched. Details are given in the user manual.

Connections to the board are either by a 28-way Spectrum-style edge connector or Soldercon pins — breadboard style. The manual gives comprehensive details of how the port works and how to set it up. It also includes two brief programs, one to make the port test itself and one to show binary numbers being output to LEDs.

One thing it does not do is to give simple circuit diagrams to show how to connect a LED or perhaps a relay. Priced at £13.50 plus 35 pence p&p, including manual, it provides a cheap introduction to control applications. Available from Multitron, 5 Milton Close, Headless Cross, Redditch, Worcs B97 5BQ. Tel: 0527 44785.

Sinclair thermals

DEAN ELECTRONICS has recently introduced a Sinclair-compatible thermal printer, the Alphcom 32. It is manufactured by the American company which produces the Timex-Sinclair 2040.

The printer plugs into the rear connector of either the ZX-81 or Spectrum and will accept the standard commands of LPRINT, LLIST and COPY, so existing software can be used without alteration. Using 110mm. wide white thermal paper, it produces a very readable output at a speed of roughly two lines per second.

The printer casing is approximately 195mm. 140mm. × 55mm. blackmoulded ABS, with a perspex blister on top which holds the paper. Two wires emerge from the back, one - about 150mm, long — to an over-size edge connector containing a 74LS10, used to decode A7 and A2, and a ferrite ring to suppress interference. It has a ZX-81 size connector to the computer and a through port for RAM packs. The other lead connects to the supplied external power supply by way of a male 3.5mm. jack plug. Inside the printer there is a minimum of electronics — a ROM chip, marked TS2040, to handle the printer operations; five chips to control the printing mechanism; a handful of discrete components; and two PCB-mounted switches to turn the printer on and off and to advance the paper. Use of both switches together performs a self-test function.

The bulk of the space is occupied by a very solid-looking, rubber-mounted printer mechanism. The printhead is made of a ceramic material into which 20 wires are inlaid. As they are moved across the paper they burn off the top surface of the paper to leave a black ink impression.

Each wire covers two character squares in a zigzag fashion which shows up the only disadvantages. When doing a COPY the zig-zag is noticable on any solid blocks of ink.

The printer becomes warm in use but that is not a problem, as there are adequate ventilation slots on the top and bottom and a large heatsink inside. On a Spectrum the edge connector lead fouls the power lead, making insertion difficult and it does not fit flush at the bottom, making the Spectrum slightly unstable.

Costing £59.95, including power supply and one roll of paper, with extra rolls of paper at only £1, the printer must be seen as a good alternative to the Sinclair printer. The Alphacom 32 is obtainable from Dean Electronics Ltd, Glendale Park, Fernbank Road, Ascot, Berkshire SL5 8JB. Tel: 0334 885661 and branches of W H Smith.

Sound made in stereo

FOR THOSE with a musical bent who have a Sinclair machine, help is at hand. Not a musical bent straightener but the Tricord from Petron is a stereo programable sound generator board in two versions, with and without an internal amplifier and speaker, for both the ZX-81 and Spectrum.

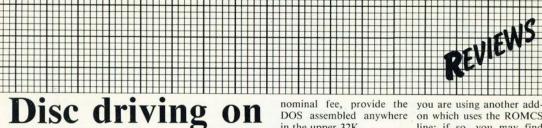
The Trichord has three basic modes of operation. First, using an in-built PROM, it can reproduce any of 255 sound effects ranging from one described as a low bong to a steam engine and whistle, plus many indescribable ones. Second, it can be used to play three-part harmony and, finally, the internal registers of the PSG chip can be accessed to produce your own sound effects.

All versions of the Trichord are the same-sized black plastic box which has a ZX-81 connector and through port. On a Spectrum that means that only a Sinclair printer could be plugged into the back of it.

Inside the box is an AY-3-8910 PSG chip. It has 14 internal registers to control the frequency and pitch of three sound channels, the pitch and channel of a white noise generator, separate volume controls, and has eight in-built envelope shapes for which the period can be altered.

The Trichord is probably the most versatile sound generator on the market at the price.

Petron Electronics is at Courtlands Road, Newton Abbot, Devon TQ12 2JA. Tel: 0626 62836.



LATEST in a sudden crop of disc interfaces for the Spectrum is the FDC-1 Mk2 from Technology Research. It will accept up to two 54in. drives in either 40- or 80-track, single- or doublesided format and is com-

the Spectrum

plete with a utility disc. The interface plugs on to the rear user port and provides a through port for other add-ons, drive cable and connector and a socket for the Spectrum power supply. On power-up the contents of an EPROM in the interface is loaded into the upper 4K of memory and a jump is made to the DOS, where a password has to be entered. The password has to agree with a password held on the disc to allow access.

At that point the full range of commands becomes available - LOAD/ SAVE of Basic or machine code, both of which can be auto-run; ERAse a file on disc; and an INITialisation routine for new discs. The initialisation is carried-out after a new disc has been FORMATed using a program on the utility disc and stores the current password on the disc.

Initialising a disc will wipe it, so an additional command LOCK is provided to prevent that hapif pening. required. DIRectory will give you provided you have the correct password - a list of the files and their length, plus the amount of spare space. Two additional commands are available for random access of the disc, PUT and GET, but our provisional copy of the new instructions gave limited details, although we are assured that will be remedied on the proper instructions.

Inside the interface are two PCBs; the lower one takes the lines across to the rear connector and holds the power socket. The upper one holds the main electronics, a disc operating chip - a 1771 - theEPROM and a good deal of buffering around the cable socket. The 1771 is a relatively old disc chip and cannot provide double density but by using two boards it is very easy to change one when, at a later stage, you need that feature. The buffering on the cable is particularly useful, as the interface uses the same standard as BBC machines and a drive can be disconnected without crashing the system.

In use, the interface proved reliable. The only time it crashed was when trying to save a program without giving it details of program length and start. In that instance the interface defaults to saving the whole 64K but the drive did not appear to like the idea and just spun aimlessly. Apart from that it worked first time, every time.

Machine code users who normally use the upper memory for their routines are catered for as Technology Research can, for a DOS assembled anywhere in the upper 32K.

With a 40-track, singlesided drive the interface gives 97.5K of file space, with 2.5K being taken by the directory. Larger-capacity drives lose a similar percentage.

Priced at £85 plus VAT, the interface is rather expensive but it allows the use of drives not dedicated to one machine; also if an 80track, double-sided drive is used, you have 390K of file space at less than £2 a time.

Technology Research Ltd, 356 Westmount Road, London SE9 1NW. Tel: 01-856 8408.

Joystick variety

NEW FROM Fox Electronics is a programmable joystick interface for the Spectrum. The interface plugs into the rear connector of the Spectrum and has a through connector for other add-ons. On the righthand side of the case is a standard 9-pin, D-type, Atari-style socket for the joystick and one switch.

To use the interface all you have to do is put up the switch, which then displays a menu on the screen. You then have the option of creating a new key set from any of the 40 keys, including the shift keys and EN-TER, or selecting, with a single keystroke, one of the 16 sets already created.

Pressing the E key exits to Basic ready to load a game and programs the joystick. If necessary, the key sets can be saved on tape.

Leaving the switch down will make the Spectrum ignore the interface unless

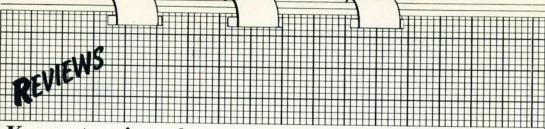
on which uses the ROMCS line; if so, you may find that a clash occurs.

Inside the interface is a CMOS RAM, the 6116LP, and a small ni-cad battery. When in use the battery is kept topped-up by the Spectrum power supply, via a 7805 regulator, and on power-down maintains the memory for a minimum of six months.

On putting up the switch the interface pages-out the Spectrum ROM and jumps to the program held in its RAM. The program then transfers into the Spectrum RAM, pages the ROM back in and puts the menu on the screen. On pressing the E key, the program transfers back to its own RAM, sets up the joystick and calls the NEW routine. Any new key sets created are saved in the process

All that is clever and provides the easiest interface on the market today but it also provides two by-products. First, when the switch is put down, the interface causes a hardware re-set. That is to say if you have a game running you can jump out of it without having to pull the plug — a saving of plug wear. Second, details are available from Fox for a machine code programmer to use the interface as a pseudo ROM. Often-used routines could then be loaded at the flick of a switch.

When used with a Microdrive, the Spectrum power lead fouls the joystick lead. making insertion difficult. At £28.50, the interface is very good value. From Fox Electronics Ltd, 141 Abbey Road, Basingstoke, Hampshire RG21 9ED. Tel: 0256 20671.



Kempston interface prints defined graphics

NEW from Kempston Electronic is the model E Centronics interface for the Spectrum. It contains an EPROM which enables it, on power-up, to direct the commands LPRINT and LLIST direct to the printer without the need for additional software. Also built into the EPROM are routines which allow the use of COPY for the Epson and Siekosha range of printers.

Housed in the standard Kempston case, the interface plugs into the user port of the Spectrum and is complete with a cable to connect to the printer.

The interface is deadended in that it does not have a through port for other add-ons. That may be a problem if you want to use it at the same time as the Kempston joystick interface or any other dead-ended device. A difficulty which may arise with full-size keyboards is that the case is shaped with a lip to fit on top of the standard Spectrum. The lip may prevent the case fitting snugly.

Inside the case is a 2K EPROM which houses the printer software and a handful of chips which detect when the Spectrum is using the LPRINT, LLIST and COPY commands. It does that, for example, with the COPY command, by monitoring an address, and when the Spectrum uses it

to do a COPY the interface takes over and directs output to the printer.

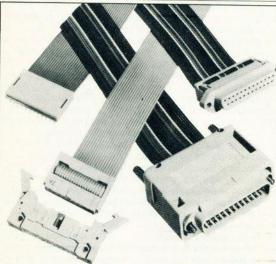
Using the interface is very simple. If only text is to be printed, no setting-up is needed, apart from POKEing an address with the number of columns required if that is other than the default setting of 80. The address is one of the unused ones in the system variables area.

To use the COPY command the interface must be set up for your type of printer. By entering as a direct command COPY: REM? the interface displays a menu page which shows its current status. You can then set it up for the Epson range, Seikosha 100 or 250 printers, or add your own routines. Once you have set it up in that way any user-defined char-

acters or graphics characters which appear in a listing will be printed as shown on the screen An annoying feature is that those characters are wider than normal and make the listing appear untidy.

For computer artists there is an enlarged setting. When it is turned on, COPY will produce a double-sized copy, about 180mm. × 145mm. on an Epson, suitable for hanging on the wall. Other settings are available to control the tokens, escape characters and automatic line feeds.

At £55 inc. the interface is by no means inexpensive but has many useful features. Details from Kempston Micro Electronics Ltd, Unit 30, Singer Way, Woburn Road Industrial Estate, Kempston, Bedford MK42 7AF. Tel: 0234 856633.



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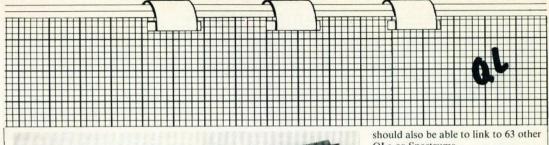
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Real computing power at a moderate cost

Mike Wright reviews the latest machine from Sinclair Research and finds much of which Sir Clive can be proud.

THE NEW MACHINE from Sinclair Research, the QL or Quantum Leap, promises to live up to its name and to be a major revolution for people in business who want real computing power at moderate cost.

The hardware is designed merely to provide large computing power at a reasonable cost but the supporting software and expansion facilities are all aimed at the professional and small business market.

INTEL 8049

The QL measures $5\frac{5}{8}$ in. \times $1\frac{3}{4}$ in. \times 183in., weighs a little more that 3lb., and can be connected to either a monitor or a television screen. A colour monitor will give a wider screen and a greater resolution than a television screen. The machine features the Motorola 68008 32-bit processor with 128K of user RAM: 32K is reserved for the screen display, which in its highest resolution gives 512×256 pixels in four colours or 26 × 256 pixels and eight colours.

An Intel 8049 is also used in the CPU controlling the keyboard, sound, RS232C receive and real-time clock functions. That leaves the 68008 to look after all the prinicipal

functions and in all there is 1MB of non-segmented address space available.

Four custom-built chips are also included. The first, dual-sourced from Plessey and Synertek, controls display and memory; the second, dual-sourced by NCR and Synertek, controls the other major functions, including the Microdrives, local area network and RS232C transmission; the third and fourth from Ferranti provide analogue functions required by the Microdrives.

There is a 65-key, full-travel keyboard. The introduction of the keyboard means that the traditional use of keywords on other Sinclair machines will not be possible. It is complete with two built-in Microdrives and the ability to connect six more. They are improved versions of Spectrum Microdrives and are not compatible with the Spectrum, although with re-formatting the cartridges will

MICRODRIVES

Other features include a ROM cartridge slot which will allow the ROM to be expanded by 32K, an expansion slot for a 0.5MB RAM, two RS232C ports and two joystick ports. The QL

OLs or Spectrums.

The cost of all those facilities is a reasonable £399 but before it can be put to use a monitor or a television set and a printer will be needed. That will add about £300 for a colour monitor and £250 for a reasonably good-quality dot matrix printer.

Even with those additions, the cost compares very favourably to existing systems. Sir Clive Sinclair said at the launch: "For £800 you can have a word processor better than anything you can buy currently."

Despite those cost-benefits there are several points which must be worrying to potential business users. The biggest of them is the decision to stay with the Microdrive, although in an updated and improved form, instead of using floppy discs as back-up storage. Since the introduction of Microdrives last year, some experts have been worried by the performance of the drives and by the use of continuous loop of video tape.

In addition, there is no connection for a cassette recorder. Although loading from a cassette is a slow, tedious business, a cassette copy of a program is usually fairly reliable and it is cheaper to produce commercial programs on cassette.

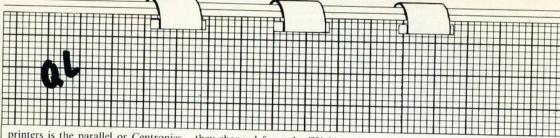
The cost of a blank cassette can be as little as 15 pence if bought in large numbers, while the costs of a Microdrive cartridge is about £5.

DISC PLAN

It is also interesting to note that Sinclair Research plans to produce a hard disc interface, while it has no plans to produce either a disc drive or a floppy disc interface. The cost of a hard disc could be two to three times the cost of the computer.

Another point to consider for users with large amounts of data is that once the RAM expansion has been fitted the OL has 640K of RAM. Each Microdrive cartridge holds only a maximum of 100K.

In keeping down the costs of the new machine, Sinclair may have sinned by omission for business users. The industry standard interface for



printers is the parallel or Centronics interface.

The QL is equipped with two RS232C ports but not a Centronics port. Most printers are fitted with a Centronics interface while the RS232C is offered as a more expensive option. An alternative would be to wait until Sinclair produces its planned Centronics interface but that again means extra cost.

For its operating systems, Sinclair Research has developed QDOS for which there are some elaborate claims. It is able to run more than one program at a time, it can divide the screen into a number of windows so that different displays can be shown simultaneously and input-output is device-independent.

The language used is another in the growing family of Basics, this one being called Sinclair Super Basic. It is said to be "a radical enhancement of Spectrum Basic." That makes for the same problems people found when

they changed from the ZX-81 to the Spectrum. They will not be able to upgrade their machines while retaining their favourite software. Even if the program is on a Microdrive cartridge, it will not be possible to use it on the QL.

The feature which will interest nontechnical users is the suite of programs written by Psion specifically for the QL. They are described by Psion managing director David Potter as "more powerful and functional than existing products for desk-top computers costing up to £5,000."

OPTIONS DISPLAYED

The suite has been designed for usability by a mass market with no prior training. It is said that even the most inexperienced person can perform useful tasks immediately, while experienced users can achieve a remarkable level of sophistication.

There is a word processor, a spreadsheet, a database and a busi-

ness graphics program. They are integrated in style, structure, design and, perhaps most important, in the sharing of information. The last feature allows data to be transferred between programs so that information from the database or spreadsheet can be transferred to the graphics program where it can be represented graphically and from where it can be moved into a document for printing.

Although the manual contains large sections on all four programs, information on the present status and options available are displayed in English at the top of the screen.

The QL appears to live up to Sir Clive's claims that it is a quantum leap for the company and computing. It is aimed at the business market and it would appear to satisfy the demands of people in business. Sinclair Research, however, appears to be hedging its bets by including joystick ports, so that games can be played on

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Counting the pins helps in making the quantum leap

THE MOST DIFFICULT job in designing the new Sinclair QL was to reduce the pin count to a satisfactory level. That is the view of one of the leading members of the design team, David Karlin.

"We spent the first two months trying to reduce the pin count to 80," he says, adding that it was one of the major reasons for choosing the Motorola 68008 for the main CPU chip. Using the ability of that chip to process information in 32 bits but having only an 8-bit bus allowed for a big reduction in the number of pins.

32/8-BIT

The chip, however, caused Sinclair Research some problems in deciding how to describe the machine. Ideally it should be a 32/8-bit machine but that is not a generally-accepted naming system and it was thought it would have been confusing.

Karlin adds that whatever it was called, all the software would look like that for a 32-bit machine.

The full CPU consists of the 68008 operating at 7.5MHz for all the principal functions, while a second processor, the Intel 8049, controls the keyboard, sound, RS232C receive and real-time clock functions.

The operating system, called QDOS and developed by Sinclair, is said to include a number of key features such as single-user multiple tasking, time-sliced priority job scheduler, display handling for multiple-screen windows and device-independent input-output.

It has 1MB of non-segmented address space, which makes possible a wide family of peripherals and enhancements. Of that, 32K is used for the screen bit map; a small amount is used for other functions leaving, on the unexpanded 128K machine, about 96K of usable memory.

The RAM can be extended exter-

nally to 640K and the 32K ROM can be expanded by ROM cartridge to 64K. The QL uses Super Basic which is said to be a great improvement on the Basic used in the Spectrum.

There are four other chips which are designed to Sinclair specifications. Two have been dual-sourced to ensure there are no difficulties with delivery. The first, which controls the display and memory, is supplied by Plessey and Sunertek. The other, from NCR and Synertek, controls the other major functions, including the two Microdrives, local area network



and RS232C transmission. The other two chips are supplied by Ferranti and provide the analogue functions required by the Microdrives.

Karlin says that once Sinclair had set the specifications for the chips discussions were started with a number of manufacturers to discover not only if they could meet the technical requirements but also if they would be able to supply the chips in sufficient numbers. Having decided on the companies, the design work was done independently.

"Rather than have one company do the design work and then supply the other with a mask, we thought it better to have each company do its own design," he says. That was an area which threatened to become one of major difficulty. When the first prototypes were received from the manufacturers they did not work in the system. "We checked them thoroughly and eventually found that there was nothing wrong with them. The problems had been external," Karlin says.

He followed the development work all the way through and listening to him talking about it would be easy to think that he had been involved in nothing more than a making a few simple improvements to the Spectrum. Many observers, however, are already saying that Sinclair Research is fully justified in saying that the new machine is a quantum leap of similar proportions to that which brought computing power within the reach of millions with the introduction of the ZX-80.

Existing machines which use similar technology cost a minimum of £3,500 and to upgrade existing micros to provide comparable facilities it is estimated the cost would be almost £2,000. The QL with its software support costs £399.

GENERAL RESEARCH

Karlin has been with Sinclair Research since August, 1982 when he returned from the States, where he had been working on general research for Fairchild in Palo Alto, California. Although born and educated in Britain, he went to the U.S. after leaving Trinity College, Cambridge, where he had been studying a mix of engineering and electrical sciences.

He decided he wanted to return to Britain, contacted Sinclair Research which was looking for researchers at the time, and was given a job. Since then he has worked on a number of projects but his main work has been on the development of the QL.

From conception to launch took only 14 months. "The first six



months were spent designing the ICs and the last eight months were needed to debug them," he says.

In that time, little changed from his original ideas of what the computer should contain. "I would have liked to include a £20 colour tube and we decided to increase the RAM to 128K at a fairly late stage but the overall design was much the same as my original ideas," he says.

He emphasises, though, that the design was a co-operative effort by a number of people at Sinclair Research.

PORTABLE MACHINE

"It was not a simple matter of Sir Clive giving us a specification and the rest of us producing a machine to satisfy that," he says. "We were all throwing around ideas, some of which we used and others we did not."

One of the early ideas, given wide publicity by Sir Clive, was a portable machine with its own power supply and flat-screen television and two Microdrives. Karlin said, however, that that had been decided against early in the development stage.

Having two Microdrives formed part of the final machine, although improvements have been made on those which are used with the Spectrum. Although much of the drives has remained the same, with storage in the region of 100K and an average access time of 3.5 seconds, Karlin says they have been "improved a great deal".

"We made a number of engineering changes, put in a great deal more error-checking and tried to increase the performance of the controller chip."

He cannot say if those improvements will also be made to the ZX Microdrives.

Karlin defends the decisions to omit two facilities, a Centronics interface and a cassette recorder connector. With the QL having a ROM cartridge slot and Microdrives, he sees no necessity for inputting information from cassette.

The reasons for including an RS232C rather than a Centronics interface were more complicated. While agreeing that the Centronics is more usual at the moment, he says:

"The Centronics interface is more expensive and occupies more board area than the RS232C but does exactly the same job. The problem is that it is the industry standard but we think that with our using the RS232C more people will start using it as well."

Karlin is confident that the QL represents 14 months well-spent. He sees an immediate market for the machine in the professions and higher education, particularly for university students who have large calculations to do or theses to write. In addition, there is the small business market where he thinks large numbers can be sold, not only because of the price but "because of the large amount of business software and the quality of that software."

PLENTY OF IDEAS

Sinclair Research is also confident about the machine and it intends to publish ROM information as soon as it can get it together from all the internal documents.

"Everything we have done has been done very carefully so that we can be confident it will work," Karlin says. "We did not publish that information about the other machines because we wanted to be able to change things if we found it necessary."

About any future work with which he is involved at Sinclair Research, Karlin is keeping very quiet. "There are always plenty of ideas being thrown around at Sinclair, so I do not think there will be any shortage of work," he says.



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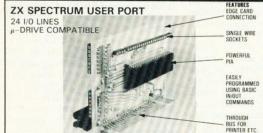
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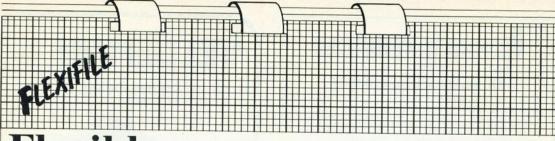


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Flexible response to growing piles of paper

Filing large amounts of information can be difficult with many of the available programs. John Davison decided to write his own to allow him to adjust the system to suit his requirements

HROUGH THE YEARS I seem to have amassed a considerable quantity of paperwork containing information on a wide variety of topics, most of which I would not want to dispose of, but to which I seldom refer. While I was anxious to keep the information, I was equally keen to reduce the volume of paper. The obvious solution was to store it on cassette tape.

In looking for a suitable file program I soon realised that most of those available, or published, had one of two failings of a pre-determined fixed length and/or a large number of relatively short records. In both respects that was the precise opposite of what was called for; I needed to store fairly long individual records and have total flexibility of record length. The answer, of course, was to write the program to suit my requirements. Flexi-File is the result.

TEXT FILES

Initially I used it to create files of historical and geographical information but given its inherent flexibility I have since used it for a variety of other types of information, including temporary text files such as the first draft of an article. There is no restriction as to its use. If you have any information in the form of a subject heading — record name — and textual details record entry, Flexi-File is for you.

In addition to reducing the sheer bulk of your filing system, Flexi-File offers basic text-handling functions. Let me make it clear now that the program is not a word processor; to achieve that on the Spectrum requires a machine code program. While I admire several features exceptional to Sinclair Basic, not least the stringhandling utilised to the full in this program, its speed is not impressive when compared, for instance, to BBC Basic. Ironically though the Spectrum offers much more user-available RAM than its competitors and it is that feature that makes it so useful for file-handling, permitting a file approaching 30K in length.

OVER-WRITE

Flexi-File offers a record displayed in a formatted style, to avoid splitting words at line ends, and with the option for a printout from a ZX printer if available; an editing facility which enables text to be inserted, deleted, or moved, and individual characters to be over-written.

I have found the program especially useful when writing notes or original text. Instead of the confusion of erasing and alterations one associates with a 'paper file' it has been simple to change the text as needed and then print-out a clean copy via the printer. It is obviously an ideal program for anyone making notes for educational purposes, be it a school project, university dissertation, or preparation for a lecture; the first draft of the various sections can be typed into the file and edited as required.

Clearly the ZX printer is no use to produce the final copy but at least the screen display offers a clean, tidy and readable draft from which the final version can be typed — no struggling with deciphering your own hastily-scribbled handwriting. Or are we, perhaps, already approaching the day when written work will be accepted on computer cassette?

The only function usually associated with file programs and not available here is a SORT routine but, given that Flexi-File is designed to hold a relatively small number of

records, that is not a significant disadvantage. If we assume an average record length of 200 words — 1,200 bytes — the file will hold 25 records and it will take the search routine about one second to locate the start of the last record in the file, which should be acceptable.

So let us look at the program in action, which should enable you to see what personal applications you have for Flexi-File. Having typed-in the program listing, carefully and correctly, enter the following as a direct command:

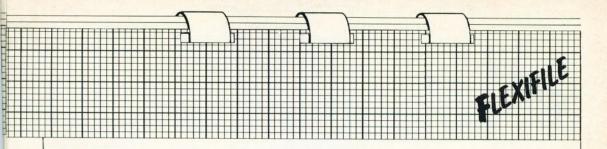
LET e\$ = "FLEXI-FILE": LET fe = 30000 : SAVE e\$ LINE 80 : VER-IFY e\$ < enter >

Once the program — about 5.7K — is SAVEd and VERIFYed, enter GO TO 80 and the menu will appear; the program is operated primarily from the menu. The BORDER and PAPER are set to blue and the INK to white, chosen because they seem to be the most pleasing to read. They can be changed easily by altering the appropriate numbers in line 80 but bear in mind that there are other colour commands in the program which assume white INK, so beware of producing invisible prompts.

FACILITIES

The facilities offered are to LOAD a file from cassette, SAVE the current file to cassette, list the record names in the current file, change one of those record names, start a new record, or review an existing record. The bottom line of the display invites you to type-in a number, I to 6, or 'n' to create a new file. So type 'n' and the screen will clear and print a warning message, appropriately in red.

That is just a safeguard in case you inadvertently press 'n' while an existing file is in the computer. Type 'c'



The Listing

10 PAUSE 0: IF INKEYS="" THEN Ch TU 10

11 LET 14-INKEY# BEEP .002,0 RETURN

40 CLS - GO SUB 600 PRINT PA PER 2:AT 0.CODE f%(n);" ":Page :TAB 27:30000-fe LET c=LEN a%:

1F c)672 THEN LET c=672 41 PRINT a\$(TO c)

42 LET line=10: LET col=16 43 PRINT AT line, col; INVERSE 1) OVER 1:

44 GO SUB 10: PRINT AT line, co

1; INVERSE 1; OVER 1;" "
45 IF 1#>"4" AND 1#<"9" THEN
LET line=line+(1#="6")-(1#="7"):
LET col=col+(1#="6")-(1#="5")

46 LET line=line-(21 AND line) 21)+(21 AND line(1): LET col=col (32 AND col)31)+(32 AND col(0) 47 IF (line-1)*32+col+1>LEN 45

THEN LET line=INT (LEN 34/32)+ LET col=LEN a\$-(line-1)*32 48 LET Posn=(line-1)*32+col+1 49 IF CODE 1\$=226 THEN RETURN

50 IF 1#="d" THEN INPUT AT 1. 0)"How many characters? ")ch: LE T as=as(TO Posn-1)+as(Posn+ch T 0 > GO TO 40

51 IF 15="s" THEN LET start=P

n: GO TO 42 52 IF 18="f" THEN LET finish= Posn: LET x==a\$(start TO finish)
: LET a\$=a\$(TO start-1)+a\$(fini

"LET 38=386 (U start=1)+384fini sht I TU) GU TU 40 53 IF i8="m" THEN GU TU 57 54 IF 18=""" THEN PRINT AT li ne.col;" "JCHR® 8) BEEP .1.20 GO SUB 10: PRINT i8: LET 38(Posm

>=i#: GO TO 42 55 IF i#<>"1" THEN GO TO 43 56 INPUT AT 0.0, "Enter new tex

t > ") LINE x4 57 LET 34=350 TU POSN-1)+x4+48 (POSN TU): GU TU 40 88 BORDER 1: PAPER 1: INK 7: G 88 BURDER 1: PRIPER 1: INK 2: G U SUB 450: PRINT e#;AT 0:16;3900 0-fe; bytes free"'''1 - LUHD a File"'''2 - SRVE the File"''''3 - List Record Names"'''4 - Chan 9e a Record Name"''''5 - Start n ew Record"'''6 - Review a Record AT 21,8; "Type number . N = Upe n New File "

81 GO SUB 10 : IF 15="n" OR 15= "N" THEN GO TO 90

82 IF i\$<"0" OR 1\$>"6" THEN G U TU 81

83 GO TO 1000*VAL 1\$

93 GO TO 1090*VAL 1\$
98 GO SUB 450 PRINT PAPER 7;
INK 23AT 10.4; "RNY EXISTING INF
ORMATION"; AT 12.7; "WILL BE LUST
WHEN"; AT 14.5; "A NEW FILE IS CRE
ATEO"; AT 21.0; " c=Continue : Oth
er kevs=Return ": GO SUB 10: IF
1\$<\rightarrow CO R 1\$=="C" THEN GO TO 80
91 GO SUB 708: DIM f#(30008):
LET fe=2: PRINT INVERSE 1; AT 10
6," * FILE IS NUM OPEN **: DOWNER

LET fe=2: PRINT INVERSE 1)AT 10 ,6)"* FILE IS NOW OPEN *" PAUSE 30: BEEP .5,20: GO TO 80

100 LET rlen=CODE f8(n-2)*256+0 UDE fs(n-1): RETURN

150 LET fs(n-2)=CHRs (INT (rlen /256)): LET f@(n-1)=CHR\$ (rlen-C ODE f\$(n-2)*256): RETURN

200 INPUT AT 0.0; ">Enter Record Name"'">">"> LINE ns: IF LEN ns>2 4 THEN CLS: GO TO 200 205 PRINT AT 12,0,"Is this the correct name?"' PAPER 2,n# 210 GO SUB 10: IF 1#K>"Y" AND 1 K>"Y" THEN GO TO 200 215 LET n#=CHR# LEN n#+n# RETU

250 LET a=n+CODE f\$(n)+1: RETUR

300 LET as="" BEEP .5,20 305 GO SUB 10: 1F CODE 1#=226 T

RETURN 310 IF CODE 18=12 THEN PRINT C HR\$ 8;" ";CHR\$ 8; LET a\$=a\$C TO LEN 35-1)

315 IF CODE 1#>31 AND CODE 1#<1 20 THEN PRINT 1\$) LET 4\$=4\$+1\$
320 IF LEN 4\$=672 THEN BEEP .1 10 BEEP .1,20 BEEP .1,30 BEE .1,20: BEEP .1,10: RETURN

325 GO TO 305 350 LET rlen=rlen+diff: GO SUB 150: LET fe=fe+diff: RETURN 400 INPUT AT 0.0; "Enter Record LINE z# LET n=5

405 IF n>fe THEN GO TO 428
410 IF f@(n+1 TO n+CODE f@(n))=
2\$ THEN RETURN

415 GO SUB 100: LET n=n+rlen: G TU 405

420 CLS : PRINT AT 6,0; "There 1 sn't a Record Name" "spelt like > "" INVERSE 1; """ | z#; "" | "; I VERSE 0 : PRINT ; AT 12,0; "Would you like a list of the Record Names? "JAT 21,0;"y = YES : oth er keys = NO": GO SUB 10: IF 18= "" OR 16="Y" THEN GU TO 3000 425 GO TO 80

450 BRIGHT 0: CLS : BRIGHT 1: R ETURN

500 IF diff>0 THEN GO TO 530 510 LET bi=start

515 LET 62=61+1000: IF 62>fe TH N LET b2=fe 520 GO SUB 550: LET b1=b2+1: IF EN

bille THEN RETURN

525 GO TO 515

530 LET b2=fe 535 LET b1=b2-1000: IF b1<start

THEN LET b1=start 540 GO SUB 550: LET b2=b1-1: IF b2<start THEN RETURN

545 GO TO 535

550 LET ts=fs(b1 TO b2): LET fs
(b1+d1ff TO b2+d1ff)=ts: RETURN
600 CLS: PRINT PRPER 2;fs(n+1 TO n+CODE f\$(n)); LET line=1 RETURN

700 CLS : INPUT "Name of FILE? : LINE es: IF LEN es>10 THEN G 0 TO 700

710 RETURN 1000 GU SUB 700: PRINT AT 10,12 INT (LEN e\$/2); "LUADING ";e\$) IN VERSE 1:AT 15:9; "START THE TAPE" VERSE 1) FT 15.9) "START THE THRE"

LUAD @\$ DATA F04 >> LET F@=256*

CODE F\$(1)+CUDE F\$(2) >> GO TO 80

2000 CLS LET F\$(2)=CHR\$ (F FNT (F e>256) >> LET F\$(2)=CHR\$ (F e>CDE
F\$(1)*256) >> PRINT INVERSE 1) AT fac) #236) PRINT INVERSE 1361 6.6) "SAVE 198" SAVE es DATA fac) BEEP .5.20 2010 GO SUB 450 PRINT AT 6.8;"

REWIND TAPE "; AT 9,4; "Press a REMIND TAPE "JAT 9.4;"Press & my key to VERIFY" PAUSE & GO S UB 450: PRINT AT 10,1;"On Error ""R" enter GO TO 2000" VERIFY e* DATA f&(): BEEP .1,10: BEEP .15,20: BEEP .15,20: GO TO 80 3000 LET n=5: LET x=0

3010 GO SUB 450: PRINT PAPER 2; e\$;" | Record Names (1 TO 32-LEN e\$): LET line=2

3020 LET a=a+(a=0)-(a=1): PRINT PAPER a;AT line,0;f\$(n+1 TU n+C UDE f\$(n)): GO SUB 100: LET n=n+

rlen 3030 IF n>fe THEN GO SUB 3100

GO TO 80 3040 LET line=line+1: IF line=22 THEN GO SUB 3100: LET line=2: GO TO 3010

3050 GO TO 3020 3100 INPUT AT 1,0,"Print-out wan ted? ", LINE is: IF is="y" OR is ="Y" THEN COPY

3110 RETURN 4000 GO SUB 450 GO SUB 400 PRI NT AT 2,0) "Current name is '-" PAPER 0''z#: GO SUB 200: GO SUB 100: LET diff=(LEN n\$-1)-LEN z\$:

IF diff=0 THEN LET fscn TO n+C ODE n#(1))=n#: GU TU 80 4818 LET start=n+CODE f\$(n)+1 O SUS 500: LET f#(n TO n+CODE n# (1))=n\$: GO SUB 350: GO TO 80 5000 CLS - GO SUB 200 CLS - PR1 NT PAPER 2) m\$(2 TO)) TAB 27:300 00-fe: GO SUB 300: LET rlen=3+CO DE n\$(1)+LEN a\$: LET n=fe+3:

SUB 150: LET f\$(fe+3 TO fe+rlen) =n\$+a\$: LET fe=fe+rlen: CO TO 80 6000 CLS: PRINT INVERSE 1;AT 8 6000 CLS - PRINT INVERSE 1,AT 8 GO SUB 100 PRINT AT 12,15-INT (LEN 2\$/2);">";2\$;"<"

GLEN 3002777 7207 GB16 LET Page=1: GU SUB 250: INP UT AT 8:0;" Edit : Format : Add : Lelete Return > "; LINE IF 15-"a" UR 15="A" THEN GU 1 % TO 8000

6020 IF 15="d" OR 15="D" THEN G O TO 9000

6030 IF 1\$="e" OR 1\$="E" THEN G 6 TO 7000 6040 IF 1\$="f" OR 1\$="F" THEN G

0 TO 6100 6050 IF 1\$="r" UR 1\$="R" THEN G

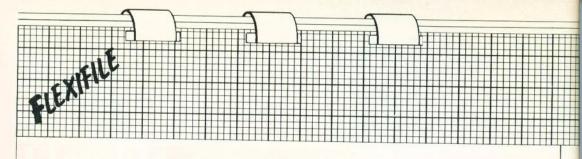
0 TO 89 6060 GO TO 6010

6100 GU SUB 600: PRINT PAPER 23 TAR 30: 0190: ". 6110 LET b=a+31: IF b>n+rlen-3 T LET b=n+rlen-3

6120 IF f4(b)<>" " AND f4(b)<>"" AND f4(b+1)<>" " AND b<>n-3+r1
en THEN LET b=b-1: GO TU 6120 6130 PRINT AT line, 0; fa(a TO b) 6140 LET a=b+1: IF a>n+rlen-3 TH EN GO SUB 6200: GO TO 6010 6150 IF f*(a)=" " THEN LET a=a+

1: GO TO 6150 6160 LET line=line+1: IF line<22 THEN GO TO 6110

6170 GU SUB 6200: LET Page=Page+ 1: GO TO 6100 6200 INPUT AT 1.03"COPY - LPRI NT > "; LINE 1#: IF 1#="C" OR 1#="C" THEN COPY



6218 IF is="l" OR is="L" THEN L
PRINT ")";f\$(n+1 TO n+CODE f\$(n)
))""'f\$(n+CODE f\$(n)+1 TO n+rle
n-3)
6238 RETURN
7608 LET b=a+671: IF b>n+rlen-3
THEN LET b=n+rlen-3
7818 LET a=f\$(a TO b): GO SUB 4
8
7828 LET diff=LEN a\$-(b-a+1): IF
diff=6 THEN GO TO 7848

7030 LET start=b+1: GO SUB 500 7040 LET f\$(a TO a+LEN a\$-1)=a\$: GO SUB 350: LET a=a+LEN a\$: IF a>n+rlen=3 THEN GO SUB 250: GO TO 6010 7050 LET page=page+1: GO TO 7000 8000 CLS: GO SUB 600: PRINT PA

7050 LET page=page+1: GO TO 7000 8000 CLS: GO SUB 600 PRINT PA PER 2:78B 27:30000-fe GO SUB 30 0: LET start=n+rlen-2: LET diff= LEN at: GO SUB 500: LET f%(start TO start+LEN at-1)=at-GO SUB 3 GO TO 60:0 9020 LET diff=rlen-rlen*2: LET s tart=n+rlen-2: GO SUB 500: LET f e=fe+diff: GO TO 80

and you are asked to INPUT the filename (e\$); as that is used in the LOADing and SAVEing routines it cannot exceed 10 characters in length and the program refuses to accept an invalid file name — lines 700–710.

The file array (f\$) is then DIMensioned 30,000 characters long and the file-end marker (fe) set at 2. That last variable is used to point to the end of that part of the file already used, or in other words fe+1 is the first free byte. It is set initially at 2 because the first two bytes of a file hold the value of fe.

fe = CODE f\$(1) * 256 + CODE f\$(2).

Once again the screen is cleared and the menu printed. This time type '5' to enter the first record. In common with most of the routines in the program, this single-line routine — line 5000 — makes extensive use of sub-routines. The first of those — line 200 — INPUTs the record name, which cannot exceed 24 characters in length, for reasons connected with the display routines. Before the name, (n\$), is accepted it is printed so that a check can be made of its spelling.

SPELLING

Most of the choices make use of the sub-routine at line 10, which obviates the need to use the ENTER key; a repeat facility is available in this sub-routine, on all keys, by holding down the key, but note that with a SHIFT-ed character the SHIFT key has to be kept held down also. Where a YES/NO response is expected, 'y' = YES and all other keys = NO.

So, if the record name is spelt correctly, type 'y' and the record name is accepted, with a control character added at the start of string —

line 215 — the CODE of this character represents the length of the name and its relevance will be apparent later.

The record name is then printed at the top of the screen on a strip of red PAPER, along with the number of free bytes in the file. As the record name has not yet been placed in the file, its length is not included in fe and so the number displayed at this stage should be 29998. The sub-routine at line 300 is then called and you can type the entry on to the screen, the entry being held in a temporary string (a\$). It is displayed on the screen exactly as it will be held in the file and individual characters may be deleted by using the DELETE key in the usual way.

TYPE THE ENTRY

A screenful of text is the most that may be entered by this routine and when the length of a\$ reaches 672 line 320 - the program BEEPs to inform you of the fact and returns to the main routine in line 5000. To return before a\$ reaches that length type SYMBOL SHIFT/A (STOP) line 305. Before the record is entered into the file its overall length is calculated (rlen), that being stored in the first two free bytes - line 150 followed by the record name (n\$) and the entry (a\$). Thus a complete record comprises the following elements:

(CODE byte 1) \star 256+(CODE byte 2) = total record length

(CODE byte 3) = length of record

(bytes 4 to x) = record name (bytes x + 1 to y) = record entry

Finally this routine adds the record length to fe and returns to the menu.

To test the program it will be worth repeating the procedure a few times, so that all functions can be examined.

END MARKER

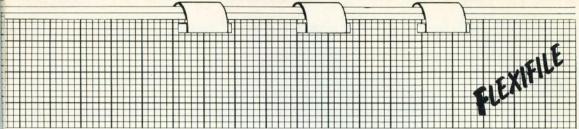
Having done that and having returned to the menu display, type '3' to list the record names. That routine prints a heading, on red PAPER, of the filename, which is presumed to be at least five characters long; if you anticipate shorter file names add extra space between the quotation marks in line 3010 and then print-out the record names alternately on blue and black PAPER.

If there are more than 20 records the listing is displayed as two pages with an option to send each page to the printer. This routine involves the search sub-routine.

Initially the variable n is set at 5—line 3000 — which is the subscript of f\$ holding the length of the first name. After that name has been printed, n is incremented by the length of the first record — lines 100 and 3030 — so that it equals the subscript holding the second name's length, and so on until it exceeds the file-end marker. Thus the time taken to locate any record depends on its position in the file; the time taken to find the 'last' record is a product of the number of records, irrespective of their individual or combined lengths.

ROUTINE

The routine at line 4000 — type '4' from the menu — may at first glance appear superfluous after all; the record name entry sub-routine gives you a chance to check that the name is correct. That routine is designed to permit a complete change of the name. I first included it as a result of



storing biographical information about fictional characters; the record name was, of course, the name of the character and in a few instances I wanted to change the name. Depending on your personal applications, this routine may or may not be used much

Given that the new record name will not necessarily be the same length as the old one, it becomes necessary to move blocks of the file up or down within f\$ to create extra space or close vacated space. The sub-routine at line 500 performs that function and is entered with the variable diff equal to the difference in length of the two names, and start pointing to the first subscript after the old name. The file is moved in blocks of 1001 bytes and the new name inserted. The record length control characters and fe are adjusted accordingly - sub-routine 350 — and the menu re-displayed.

The principal routine of the program is that entered by typing '6' from the menu, starting at line 6000, the Review Routine. You are asked to INPUT the name of the record to be reviewed and when that has been located in the file, five options are displayed in the lower part of the screen - Edit, Format, Add, Delete and Return.

The choice is made by INPUTing, in either lower- or upper-case, the initial letter — which appears in IN-VERSE VIDEO, line 6010; at the risk of stating the obvious, when typing-in the program those letters are preceded by CAPS SHIFT/4 and followed by CAPS SHIFT/3. As you would expect 'r' is used to quit the routine and returns you to the menu.

'E' takes you into the editing facility - lines 7000-7050 - and begins by assigning appropriate values to a and b to create a temporary string, a\$, comprising a maximum of 672 characters from the chosen record.

The display has the record name, 'page' number and number of free bytes as a heading, on red PAPER, followed by the text held in a\$; the editing cursor is printed at the centre of the screen. The cursor effectively inverts the INK and PAPER colours in its current position and hence shows up clearly amid a screen of text.

The editing procedures are contained in a sub-routine starting at line 40, which was inspired by a useful routine published in Sinclair Projects, January, 1983. Strictly speaking, as the sub-routine is called only from line 7010 it should have been incorporated into the main routine but I included it in several programs and have become accustomed to it being 'GOSUB 40', so did not bother to renumber it for this particular use. The main point is that it does not detract from usefulness of the program.

FORGOTTEN

While in the sub-routine — lines 40 to 57 — in the main file is forgotten; all operations work on a\$. The functions available are deleted, insert, move, and replace. The cursor is moved by using the cursor control keys — 5 to 8 — but UNshifted. Line 46 treats each line and column of the display as if it were a loop; so, for instance, moving the cursor down from the bottom line causes it to reappear on the topmost line of the same column, and vice versa.

That means that it is sometimes quicker to reach a particular character by apparently moving in the opposite direction. That is possibly as clear as mud, in which case I suggest that you play with the cursor and you will soon see the point I am making.

Having positioned the cursor, typing 'd' — delete, line 50 — produces a prompt in the lower screen asking you to INPUT the number of characters to be deleted, the first one being at the cursor position. Almost instantaneously, the screen is re-displayed with the selected characters removed

from a\$.

To move a block of text, begin by moving the cursor to the first character of the block and type 's' - start, line 51. Then move the cursor to the last character of the block and type 'f' - finish, line 52. As with the delete function, a\$ is re-printed with the block of text removed. That block is stored as x\$ and can be moved to any 'page' of any record in the file, or indeed in any other file.

There is one very important proviso, that neither the block-move nor the insert functions should be used again until the block has been reinserted, otherwise it will be lost. From another viewpoint that can be useful, as move can be utilised effectively to delete the block of text quicker than counting the characters. To re-locate the block position the cursor and type 'm' - line 53 - the block will be inserted immediately before the cursor-position and a\$ reprinted.

To replace an individual character, position the cursor at the character and type 'r' - line 54; a space is printed over the character and a BEEP invites you to type-in the new one, which is printed into the space and inserted at the appropriate position in a\$.

If the key pressed was 'i', a prompt appears in the lower screen asking you to type-in the new text insert line 56 - and when ENTER is pressed that will be inserted immediately before the cursor position as with the block move.

COMPARED

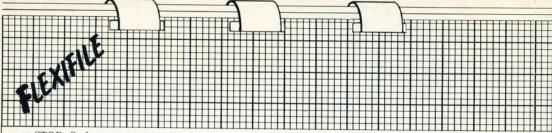
When the editing of a particular 'page' is complete type STOP (SYM-BOL SHIFT/A). The length of a\$ is then compared to its original length line 7020 - and if it is different the sub-routine at line 500 is called to move the file up or down to accommodate the new version.

The record control characters and fe are increased or decreased accordingly. The variables a and b are then incremented and the next 'page' of the record displayed.

If during that routine you should delete some text by mistake, do not

Summary of main variables

- the filename (<11 characters long) e\$:
- fS: the file itself (up to 30000 characters) fe: the file-end marker (stored as f\$(1 to 2))
- n\$: record name (<25 characters long)
- text being entered or edited as:
- character returned from INKEY\$ subis: routine (line 10)
- tS: a temporary string holding up to 1001 characters, used during the 'file-shift' sub-routine.



press STOP. So long as you are still within the sub-routine, only a\$ is being affected; the file (f\$) remains in its original form. Instead, press CAPS SHIFT/SPACE — BREAK — and then enter GO TO 7000 to start the editing function.

PAGE NUMBER

After the final 'page', the main review routine prompt appears in the lower screen. Having edited a record you will probably want to see it formatted, so type 'f'. The format function — lines 6100 to 6220 — also prints a heading on red PAPER, comprising the record name and a page number. Note that any given record may produce more pages in that form than in the editing style, because on average a formatted screen will contain only 620 characters from the file, as against a full editing display of 672.

Also note that the PRINT statement in line 6100 assumes that no record will produce more than nine pages; if you think that may not be the case, change that statement to read — PRINT PAPER 2; TAB 29+(page<10);

The record is then displayed one line at a time, each line being checked to ensure that no words carry-over from one line to the next. Each time the screen is filled there is the option to make a hard-copy on the ZX printer. Typing 'c' - COPY - does precisely that, printing-out reproduction of the screen; 'l' -LPRINT - provides a complete printout of the record but that will be unformatted. When the whole record has been displayed the main prompt re-appears.

This time, enter 'a' and the singleline routine — 8000 — enables you to enter an addition to an existing record. This screen display is the same as when starting a new record and the new text is added to the end of the existing entry, for which reason the first character to be typed should almost certainly be a space, which will save you that portion of editing later

To enter the text, type STOP (SYMBOL SHIFT/A); this has been

used on a number of occasions where you might have expected ENTER to have been used. I decided against using that key because of its proximity to the space key, which increases the chance of catching it accidentally with potentially-irritating if not disastrous consequences.

Again, the sub-routine at line 500 is used to move the file down to make room for the extra text and the appropriate control characters and fe adjusted.

The last function to consider is selected by entering 'd' — Delete, line 9000+. That function displays the record name and asks if you wish to delete the record, a safeguard in case 'd' was entered in error. Assuming you do, the file is shifted up to overwrite the unwanted record and fe decreased by the record length.

If the record was the last one in the file, it just decreases the value of fe and the record will be over-written when a new record is entered. The function returns you direct to the main menu — line 80 — as no more operations are possible on a non-existent record.

It is probably worth noting the two escape routes, to avoid a state of panic should you feel that you have entered something incorrectly and are about to lose hours of careful typing of valuable information.

BREAK

Whenever the sub-routine at line 10 is being used, typing CAPS SHIFT/SPACE, (BREAK), will produce report code L — BREAK in program. This can be especially useful during the editing routines. When a letter or name is being INPUT typing CAPS SHIFT/6 — cursor down — produces report code H — STOP in INPUT — from which you can enter an appropriate GO TO statement or examine the file via direct commands.

When a file has been reviewed fully you will obviously want to SAVE it on to tape cassette. Most file programs use a separate array to store record names, which requires you to save the program with each file. That is the main advantage of Flexi-File. By incorporating record names, and

the file-end marker, in the main file array it becomes possible to store files separate from the program, with a small saving in the time it takes to LOAD and SAVE any given file.

Typing '2' from the menu selects the single-line routine at line 2000 which, having SAVEd the file array, BEEPs and prints an appropriate message to prompt you to re-wind the tape to VERIFY the file. Immediately prior to SAVEing the file, the current value of fe is inserted into the first two bytes of f\$.

There is obviously no need to SAVE the program. To LOAD a file back into the computer, type '1' from the menu display. The routine at line 1000 begins by asking you to input the file name and refuses to accept one longer than 10 characters; no file can have been SAVEd with a name longer than that. Once the file is LOADed, the appropriate value of fe is calculated from the first two bytes of the file and the menu re-displayed.

APPLICATION

Having tried all the routines on your test data, you should have a good idea of the flexibility and application of the program. Although written for, and so far used only on tape cassette, there is no reason why Flexi-File should not be used with the Microdrive.

Using the EDIT routine to move a block of text from one file to another is clearly a very slow process with cassette — possible but not very practical.

Given the speed of the Microdrive, however, it becomes a potentially very useful function, with up to three files on one cartridge. Of course, it will become possible to amend the program to take full advantage of the improved facilities offered by the Microdrive.

If, like myself to date, you have not received an order form for the Microdrive and Interface, you can at least still make a start on creating useful files with the benefit of full textediting and know that they can be transferred to the mass-storage system when they are available.



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EXPORT PRICES ON APPLICATION



Parallel interface for the Spectrum

Many people want to connect their machines to a better printer than that supplied by Sinclair. Richard Sargent explains the design of a one-chip interface which has extra facilities.

PECTRUM OWNERS who would like to attach their machines to a printer other than a Sinclair have a choice of commercial interfaces but they are somewhat expensive. If you are intending to make occasional use of a club printer, £40 to £50 is a great deal to tie up in a printer interface. Alternatively, it you are about to buy a printer, £50 is a nasty hidden extra, somewhat worse than VAT. The answer, of course, is to do it yourself, at a cost of less than £10

The easiest printer interface to make is one which sends eight bits at a time, in parallel, direct to the printer. There is a standard set for it called the Centronics standard, where voltages, signals and pin numbers are all specified. Figure three shows the connections on the Amphenol-type plug which usually is used to plug into the printer. Figure two shows the minimum number of signals which must be exchanged between printer and computer.

The chip chosen to communicate with the printer is the Z-80 parallel input/output port chip, known as the Z80APIO. You will need to obtain the version with the "A", since that is the high-speed version. There are many clever things the chip can do

and the technical manual published by Zilog is well worth having if you think you are likely to experiment using the chip.

For our purposes, however, it is its ability to connect to the Spectrum bus with no decoding chips and its ability — on the A port lines — to have some bits configured as inputs and other bits configured as outputs, which makes it such an attractive chip. Figure three shows the connections, with the dashed section being optional.

The prototype was squeezed on to a piece of Veroboard 10cm. by 3.5cm. which did not leave much room for the wiring. The board should not be bigger than 10cm. by 6.5cm., or it will not fit into its neat cassette tape box. The cassette box not only looks neat but protects the circuitry and provides a convenient place for the operating instructions — on the inside of the clear plastic.

Figure four shows the general method of construction. The edge connector should stick out as much as possible, as that permits air to continue to flow from the back of the Spectrum, helping to cool it. A wiring diagram is not relevant — you should list all the connections you need to make and cross each from the list when you have soldered it, double-

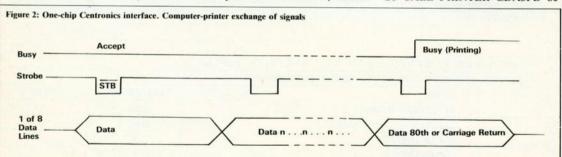
checking as you go that you have wired to the correct pin.

For the cable to the printer I used 12-core flexible cable; that is easy to anchor inside the cassette box but ribbon cable would be equally suitable.

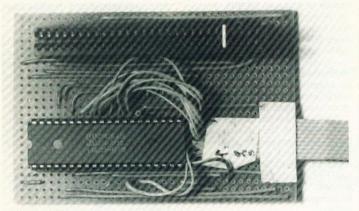
When Sinclair Basic does LPRINT and LLIST it first checks a location in RAM to find the address of a routine in ROM which it will use. That address is 15 bytes after a starting address given by the contents of CHANS. It will move around depending on how many Microdrives are fitted but CHANS is always at 23631. Normally the two bytes of RAM contain 09F4H but they must be changed to FE80H for the Centronics LPRINT and LLIST to work.

NEW and USR 0 both set them back to 09F4H. That setting-up procedure can be done from Basic, or automatically by PRINT USR 64512. COPY can be achieved only by RANDOMIZE USR 64986, and should not be attempted via the Spectrum COPY key.

The code rides in high memory and is about 700(d) bytes long. Since it is assembled for a 48K Spectrum, 16K owners will have to subtract 8000H from all the absolute jumps and calls. So CALL PRINTER CDA3FD be-



CENTRONICS INTERFACE



comes CDA37D. Five other adjustments from FD to 7D are also required, at FD8C, FD36, FD3A, FD60, FD67. Those bytes reference tables.

If space is at a premium only the last 305(d) bytes of code need be entered. That will give the COPY, LPRINT and LLIST but all Spectrum graphics will be printed as spaces and the width of print will be the maximum the printer allows — usually 80 characters. If you can enter only this minimum system, which starts at

FDA3H, you must change all four CALL OUTBYTE(CDF3FC) to CALL PRINTIT (CDCCFD). All the bytes down memory of FDA3 give the printer routine a few more facilities.

Many dot matrix printers will output block graphics, though they probably will not have the same codes as the Spectrum graphics. If you patchin the routine CHUNKY, the Spectrum graphics codes 128-143 will be changed into the codes required to output the small shape of a graphic block on an Epson 80 printer. The

routine merely looks up the new code in the table NW so if you have a printer with different codes for the various shapes of graphics, all you need is your own table of codes at NW.

The difficulty with having userdefined graphics and block graphics printed as spaces is that you ought to draw them by hand later. Patch-in the routine NUMBER and they will be printed as their decimal code equivalent, bracketed neatly.

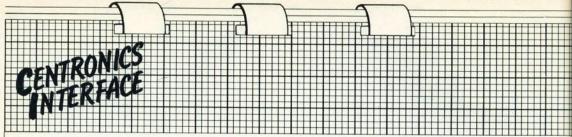
If you wish the print width to be less than the maximum specified by the printer in use, the byte 64753 must be POKED to non-zero and the width, in characters, POKED into 64754

The hexadecimal dump is not a patch but an extra routine — two routines, in fact. Any area of memory will be sent to the printer from a specified starting address — POKE 64664/5 with the start — to a specified end address — POKE 64666/7 with the finish.

With 64754 set to width 48, RAN-DOMIZE USR 64668 prints a hexadecimal dump. RANDOMIZE USR 64672 prints the bytes as ASCII but no check is made for control characters and it is recommended that you

Figure 4: One-chip Centronics interface (Spectrum). Mechanical construction.

The Veroboard is held in place by two screws, one of which anchors the cable. The edge-connector is offset to the right, so that the unit, once fitted, does not interfere with the power supply socket of the Spectrum. Note that the copper side of the Veroboard faces the Spectrum keyboard.



do not operate this printout unless you are sure your are printing a valid ASCII file.

The copy routine at FDDAH works by printing each pixel dot of the Spectrum screen as a single dot on the printer paper. A good deal of bitmanipulation is necessary to change the format of bits on the screen into the form required by the printer. The maximum size of printout obtainable from the Epson MX70/80/100 series printers is 11cm. wide, 6cm. deep. The width, but not the depth, can be halved by changing the byte at FE50H from 4B to 4C. Doubling the 5.5×6 cm. image by a software routine would then give 11cm. by 12cm. - interesting but distorted.

The new Epson RX80 printer has a graphics printout, CRT1, which prints normally as 7.5cm, wide by 6cm. deep and is capable of being doubled by software to 16 × 12cm. To do that the routine DOUBLE at FC25H is patched into the main copy

routine at location FE2EH. Further bit manipuation is carried-out to print four dots on the paper for every single pixel dot on the screen.

The code may be entered using a hex-loader, and working through the code shown in figure one. If you have an assembler, you should enter the source code and customise the program to suit your requirements.

Finally there is the matter of the six spare ports on the PIO chip. They exist, so why not use them? Five have been configured as inputs, so they could be North, South, East, West and Fire signals from a switch-type joystick. They would not, however, be compatible with any commercial software.

There is another rather more intriguing possibility — to use them to communicate with another computer. In many schools the Research Machines 380-Z operates in close proximity to a Spectrum. Often the 380-Z can send only seven bits of parallel

data to its printer and therefore cannot print-out graphics. Linking a 380-Z to a Spectrum could not only let the 380-Z print out its graphics but could, with a small amount of software, allow the Spectrum to act as a printer buffer, printing 32K of 380-Z data while the 380-Z was engaged in more productive tasks.

The intended link is one-way only, 380-Z to Spectrum. The PIO chip is configured ready for the link and the hardware requirement is merely the wiring of a Cannon D-type plug, as shown in the wiring diagram.

Two short machine code routines are needed, one for each machine, and the source listings are given. Time has not allowed me to test the code for the 380-Z part of the link but I have a similar link running successfully between a Nascom 2 and a Spectrum. It may be an expensive way to link a Spectrum but for the cost of a piece of cable and a D plug, it is certainly an interesting experiment.

Figure 3: One-chip Centronics interface (Spectrum) circuit diagram with pin nos. The layout of the Spectrum edge connector is given on page 160 of the Spectrum manual. Centronics interface 36 pin Amphenol plug 2 × 23 Way 2 × 28 Way Optional link to 380 Z Spectrum Edge Connector 31 30 31 19 DO BO 87 AO DO D2 40 39 D3 21 Strobe Z80A 10 D4 5 DO 38 D5 18 06 D2 2 D7 D7 19 D3 6 A5 A/B 23 GND 5 CON/DATA A6 A7 M1 . CF 36 IORQ IORO 35 RD RD CK CK 23 INT INT 25-pin Cannon D plug 26 + 5v 5v Ov Ov E1E0 BRBS ARAS 10K 24 21 18 22 17 Not connected

24



SAMPLE PRINTOUTS

A COPY of a screen listing.

100>REM 1234567890123456789012345678901234567890123456789012345678901234567890123456789012345678901234567890123456789012345678901234567890123456789012345678901284567890128456789012845678901287864986

A hex dump - line width now set to 48d.

F3 AF 11 FF FF C3 CB 11 2A 5D 5C 22 5F 5C 18 43 C3 F2 15 FF FF FF FF FF 2A 5D 5C 7E CD 7D 00 D0 CD 74 00 18 F7 FF FF FF C3 5B 33 FF FF FF FF C5 2A 61 5C E5 C3 9E 16 F5 E5 2A 78 5C 23 22 78

Graphics printed as their code numbers.

[129][130][131][132][133][134][135][128][142][14 1][140][139][138][137][136][143][144][145][146][147][148][149][150][151][152][153][154][155][156][157][158][159][160][161][162][163][164]

AS COPY of a screen list, in RX80 CRT graphics, doubled.

10>LPRINT " - BAT LAND A BABC DEFGHIJKLMNOPQRSTU" 100 LIST 10 1000 RANDOMIZE USR 64986

0131				FC39			
	- CIRI CINI	CTION POPOLIEL E	DINTED INTERFACE	FC3A 3E1B	PARAMS	LD A, 1BH	
0131	;FULL FUNCTION PARALLEL PRINTER INTERFACE			FC3C CDA3FD		CALL PRINTE	ER .
0131	; R SARBEN	T July 1983		FC3F 3E2A		LD A. "*"	
131		- Control of the Cont		FC41 CDA3FD		CALL PRINTE	R
C00		ORG 64512		FC44 3E04		LD A.4	
C00		LOAD 64512				CALL PRINTE	P
C00				FC46 CDA3FD		LD A,OOH	
C4F	CHANS	EQU 23631		FE49 3E00		CALL PRINTE	CD.
0096	TAB1	EQU 150		FC4B CDA3FD			0200H bytes sent
5B00	BUFFER	EQU 23296		FC4E 3E02			
5B00				FC50 CDA3FD		CALL PRINT	ER
FCOO 2A4F5C	INIT	LD HL, (CHANS)	:Find vector FC00H=64512	FC53 C9		RET	
FE03 110F00	*****	LD DE,15	and load it with	FC53			
FC06 19		ADD HL, DE	:new address	FC54 0600	B_BUFF		=256 (Buffer length)
FC07 44		LD B,H	***************************************	FC56 21005B		LD HL, BUFF	ER
FEOB 4D		LD C.L		FC59 5E	B LPB	LD E, (HL)	:Begin to transfer
FC09 1180FE		LD DE NEWPRINT		FC59	-		;contents of buffer
		LD (HL),E		FC5A CB23		SLA E	
FCOC 73				FCSC CB23		SLA E	
FCOD 23		INC HL		FCSE CB23		SLA E	
FCOE 72		LD (HL),D	1 111 11 - DID - NO	FC60 CB23		SLA E	
FCOF CDBBFD		CALL PIO	;Initialise PIO chip			LD C.4	Bit manipulation counter
FC12 CD6FFE		CALL CR_LF	;and clear printer's	FC62 0E04	B LP9	SLA E	;load carry bit from E
FC12			;own buffer	FC64 CB23	B_LF4	PUSH AF	;save carry bit
FC15 C9		RET ;>To BA	SIC, vector addr in BC	FC66 F5		RL D	:fill D from carry
FC15				FC67 CB12		POP AF	repeat
FC16 3E1B	E ROUTINE	LD A, 18H	;FC16H=64534	FC69 F1			once
FC18 CDCCFD	III TO THE SECOND SECOND	CALL PRINTIT		FC6A CB12		RL D	:Do 3 more
FC1B 3E45		LD A, "E"		FC6C OD			
FC1D CDCCFD		CALL PRINTIT		FC6D 20F5			9 ;manipulations
FC20 3EOD	C ROUTINE	LD A,ODH	;FC20H=64544	FC6F 7A		LD A,D	;Ready,
FC22 C3FFFC	-1100	JP SEND : and t	hen back into BASIC	FC70 CDA3FD		CALL PRINT	ER ;print result
FC22 CSFFFC		or sere june	1801 - 400 - 500 - 500 - 500	FC73 CDA3FD		CALL PRINT	
FC22				FC76 23		INC HL	;Step along Buffer
				FC77 10E0		DJNZ B_LPE	Repeat for whole buffer
FC22	100-0	: 512 DOTS ACRO	CC THE PAGE	FC79 C9		RET	
FC22			33 THE THUE	FC79			
FC22		GRAPHICS		FE7A 0600	T BUFF	LD B.O :1	3=256 (Buffer length)
FC22	: ******			FC7C 21005B		LD HL.BUFF	
FC22	Columbia Colonia and	Continues a Cartesia	COOK! LAKED	FC7F 5E	LP8	LD E, (HL)	:Begin to transfer
FC25 C5	DOUBLE	PUSH BC	;FC25H=64549	FC7F SE			contents of buffer
FC26 CD3AFC		CALL PARAMS		FCBO OEO4		LD C.4	:Bit manipulation counte
FC29 CD7AFC		CALL T_BUFF			LP9	SLA E	;load carry bit from E
FC2C CD6FFE		CALL CR_LF		FC82 C823	L. 4	PUSH AF	;save carry bit
FC2F CD3AFC		CALL PARAMS		FCB4 F5			;fill D from carry
FC32 CD54FC		CALL B_BUFF		FC85 CB12		RL D	
FC35 CD6FFE		CALL CR_LF		FC87 F1		POP AF	repeat
FC38 C1		POP BC		FC88 CB12		RL D	touce
FC39 C9		RET		FCBA OD		DEC C	;Do 3 more



```
Program continues
                                                                                                                                                     FD1A 280F
FD1C 18E3
                                                                                                                                                                                                       JR Z S4
JR S1
                                                                                                                                                     FD1C TBES
FD1E 7D
FD1F FEOD
                                                     JR NZ LP9 ;manipulations
LD A,D ;Ready,
CALL PRINTER ;print result
CALL PRINTER ;twice
INC HL ;Step along Buffer
DJNZ LPB ;Repeat for whole buffer
BGT
     FCBB 20F5
                                                                                                                                                                                                      LD A,L
CP ODH
JR NZ S3
XOR A
LD (PRHEAD),A
                                                                                                                                                                                  52
     FCBD 7A
     FC8E CDA3ED
                                                                                                                                                      FD21 2005
    FC91 CDA3F
FC94 23
FC95 10E8
FC97 C9
                                                                                                                                                      FD23 AF
                                                                                                                                                     FD24 322FFD
FD27 7D
FD28 CDCCFD
                                                                                                                                                                                                       LD A.L
CALL PRINTIT
    FC97
                                                                                                                                                     FD2B E1
                                ; ADDITIONAL ROUTINES -- HEX & ASCII DUMP
     FC97
                                                                                                                                                     FD2C C9
    FC97
FC97
FC97
FC97
                                                                                                                                                     FD2C
                                                                                                                                                     FD2D 0000
FD2F 0000
                                                                                                                                                                                                      DW 0 ;For internal use
DW 0 ;For internal use
                                                                                                                                                                                  PRHEAD
                                ; (PDSIT) MUST BE LOADED WITH START ADDRESS
; (FINISH) LIKEWISE WITH END ADDRESS
; USE BASIC TO DO THIS
                                                                                                                                                      FD2F
    FC97
                                                                                                                                                     FD2F
    FC97
FC97
FC98 0000
FC9A 4000
                                                                                                                                                     FD2F
                                                                                                                                                     FD2F
                                                                                                                                                                                  CODE TO DUTPUT CHARACTER CODE NUMBER IN THE FORM [1xx] INSTEAD OF
                                START
                                                                            ;Or load from BASIC FC98H=64664
;Or load from BASIC FC9AH=64666
                                                                                                                                                                                  ; A SINGLE SPACE
                                FINISH
                                                                                                                                                     FD2F
    FC9A
                                                                                                                                                     FD2F
    FC9C 0601
                                HEX
                                                                                                                                                     FD2F
                                                    LD B,1
                                                                                      :FC9CH=64668
   FC9E 1802
FCAO 0600
FCA2 2A9BFC
FCAS 222DFD
FCA8 AF
                                                    LD B,1
JR BYTES
LD B,0
LD HL, (START)
LD (POSIT),HL
XOR A
LD (PRHEAD),A
                                                                                                                                                    FD31 2600
FD33 6F
FD34 1178FD
FD37 DD216EFD
                                                                                                                                                                                                      LD H,O
                                                                                                                                                                                                                                         :FD31H=64817
                                                                                                                                                                                                     LD L,A
LD DE,RAM
LD IX,SUM
LD A,2FH
LD C,(IX+O)
LD B,(IX+I)
                                ASCII
                                                                                     ;FCA0H=64672
                                BYTES
                                                                                                                                                    FD3B
                                                                                                                                                               3E2F
   FCA9 322FFD
FCAC 2A9AFC
                                                                                                                                                    FD3D DD4E00
                                                                                                                                                    FD40 DD4601
FD43 C601
FD45 ED42
FD47 F243FB
                                                   LD (PRHEAD),A
LD HL, (FINISH)
LD DE, (POSIT)
OR A
SBC HL,DE
JR Z ALLDONE
                               LP
   FCAF EDSB2DFD
FCB3 B7
FCB4 ED52
FCB6 2825
FCB8 EB
                                                                                                                                                                                                     ADD A,1
SBC HL,BC
JP P LP1
ADD HL,BC
LD (DE),A
INC IX
INC IX
                                                                                                                                                    FD47 F243FD
FD4A 09
FD4B 12
                                                   JR Z ALLDONE
EX DE,HL
DR B ;tes
LD A,(HL)
INC HL
LD (POSIT),HL
JR Z ASC
Process for HEX
PUSH AF
SRL A
                                                                                                                                                    FD4C DD23
FD4E DD23
   FCB9 BO
                                                                           ;test HEX or ASCII
                                                                                                                                                    FD4E DD2
FD50 13
   FCBA 7E
FCBB 23
FCBC 222DFD
FCBF 2817
                                                                                                                                                                                                     INC DE
DEC C
JR NZ LP2
                                                                                                                                                    FD51 OD
FD52 20E7
                                                                                                                                                    FD52 20E7
FD52
FD54 3E5B
FD56 CDF3FC
FD59 3E31
                                                                                                                                                                                                     joutput code number as [1xx]
LD A, "["
CALL OUTBYTE
LD A, "1"
CALL OUTBYTE
   FCBF
  FCBF
FCC1 F5
FCC2 CB3F
FCC4 CB3F
FCC6 CB3F
FCC8 CB3F
FCCA CDE3FC
FCCD F1
                                                   SRL A
SRL A
SRL A
                                                                                                                                                   FD59 3E31
FD58 CDF3FC
FD5E 3A78FD
FD61 CDF3FC
FD64 23
FD65 3A7CFD
FD68 CDF3FC
                                                    SRL A
                                                                                                                                                                                                     LD A, (RAM2)
                                                                                                                                                                                                     CALL OUTBYTE
                                                                                                                                                                                                    INC HL
LD A, (RAMI)
CALL OUTBYTE
JP C3
   FCCD F1
FCCE CDE3FC
FCD1 3E20
FCD3 CDFFFC
                                                   CALL FORM
                                                   LD A,20H
CALL SEND
JR LP
                                                                                                                                                   FD6B C397FE
                                                                                                                                                   FD6B
   FCD6 18D4
                                                                                                                                                  FD6B
FD6E 1027EB03
FD72 64
FD73 000A0001
FD77 00
FD78 000000
   FCD6
                                                                                                                                                                               SUM
  FCD6
                                                   :Process for ASCII
CALL SEND
JR LP
                                                                                                                                                                                                    DB 10H, 27H, 0E8H, 3, 64H
  FCDB CDFFFC
FCDB 18CF
FCDB
                                                                                                                                                                                                   DB 0,0AH,0,1,0
DB 0,0,0
DB 0
DB 0
                                                                                                                                                   FD7B 00
FD7E 00
  FCDD 3EOD
                                                                                                                                                                               RAM2
RAM1
                              ALL DOME
                                                  LD A,ODH
  FCDF CDFFFC
FCE2 C9
FCE2
FCE3 E60F
                                                  CALL SEND
RET ;--> TO BASIC
                                                                                                                                                   FD7C
FD7C
                                                                                                                                                   FD7C
                                                                                                                                                                                PRINT CHUNKY GRAPHICS ROUTINE
                                                  AND OFH
CP OAH
JR C F2
                              FORM
                                                                                                                                                   FD7C
  FCE5 FE0A
FCE7 3802
FCE9 C607
FCEB C630
FCED CDFFFC
                                                                                                                                                   FD7C
                                                  ADD A,7
ADD A,30H
CALL SEND
                                                                                                                                                   FD7C
                                                                                                                                                                               ;If A not in range 128-143 inc then JP C5
;else load Acc and JP EXIT
                              F2
                                                                                                                                                   FD7C
                                                                                                                                                  FD7C
FD7D FE80
FD7F DA8CI
FD82 FE90
  FCFO C9
                                                                                                                                                                                                   CP 128 ;FD7D
JP C C5
CP 143+1
JP NC C5
;output block graphics
  FCFO
                                                                                                                                                                                                                                    :FD7DH=64893
                                                                                                                                                             DABCFE
  FCFO
                              ; ADDITIONAL CODE — used by hex/ascii dumps ; may also be used by llist & lprint
  FCF0
FCF0
FCF0
                                                                                                                                                   FD84 D28CFE
                                                                                                                                                  FD94
                                                                                                                                                  FD87 1600
FD89 5F
                                                                                                                                                                                                   LD D,O
LD E,A
LD HL,NW-128 ;address of newcode
DR A
ADD HL,DE
  FCF1 00
                                                  DB 0 ;if 0 then width=total width of
                             LATCH
                                                                                                                                                  FD89 5F
FD8A 2113FD
                                                                                             h=total width to
FCF1H=64753
paper FCF2H=64754
  FCF 1
                                                 printer used FCF1H=60
DB 70 ;Eg:70d for A4 paper
;Eg:48d for a hex dump
LD C,A ;FCF3H=64755
                                                                                                                                                  FDBD B7
FDBE 19
FDBF 7E
FD90 C3CFFE
  FCF2 46
                             WIDTH
                                                 LD C,A
LD A, (LATCH)
OR A
LD A,C
JP Z PRINTIT; thus CALL OUTBYTE acts
; as CALL PRINTIT
CALL OUTBYTE acts
                             OUTBYTE
                                                                                                                                                                                                   LD A, (HL)
JP EXIT
 FCF4 3AF1FC
FCF7 B7
                                                                                                                                                  FD90
                                                                                                                                                  FD93 A0A2A1A3
 FCF8 79
 FCF9 CACCFD
                                                                                                                                                  FD97 ABAAA9AB NW
                                                                                                                                                                                                   DB OACH, OA2H, OA1H, OA3H, OA8H, OAAH, OA9H, OABH
                                                                                                                                                  FD9B A4A6A5A7
FD9F ACAEADAF
FDA3
01A3
                                                                                                                                                                                                   DB 0A4H,0A6H,0A5H,0A7H,0ACH,0AEH,0ADH,0AFH
                                                                                                                                                                               EXTRAFIN
                                                                                                                                                                                                  EQU #
EQU EXTRAFIN-INIT
 FCFC C3FFFC
 FCFC
                                                                                                                                                                               EXTRA
 FCFC
FCFC
FCFF E5
FD00 6F
                                                                                                                                                  01A3
                                                                                                                                                                               This is the start of the code for the smallest system (Copy,Llist,Lprint)
                                                                                                                                                  01A3
                                                                                                                                                  0143
                                                 PUSH HL
LD L,A
LD A, (WIDTH)
INC A
                           SEND
                                                                                   ;FCFFH=64767
                                                                                                                                                  01A3
01A3
01A3
 FD00 GF
FD01 3AF2FC S1
FD04 3C
FD05 67
FD06 3A2FFD
                                                                                                                                                                                                       PRINTER ROUTINE
                                                                                                                                                                               OUTPUTS: B BITS DATA + 1 BIT STROBE;
;INPUTS: 1 BIT READY SIGNAL;
                                                                                                                                                  01A3
                                                 LD H.A
                                                 LD H,A
LD A, (PRHEAD)
INC A
LD (PRHEAD),A
CP H
JR NZ S2
CALL CR_LF
XOR A
                                                                                                                                                  0143
                                                                                                                                                 01A3
01A3
005F
007F
FD06 3A2FFD
FD09 3C
FD0A 322FFD
FD0D BC
FD0E 200E
FD10 CD6FFE
FD13 AF
FD14 322FFD
FD17 7D
                                                                                                                                                                              P_A_CON
P_B_CON
P_A_DATA
P_B_DATA
                                                                                                                                                                                                   EQU SEN
                                                                                                                                                                                                  EQU 7FH
EQU 1FH
EQU 3FH
                                                                                                                                                 001F
                                                                                                                                                003F
003F
FDA3 F5
FDA4 F5
FDA5 CDBBFD
                                                XOR A
LD (PRHEAD),A
LD A,L
CP ODH
                                                                                                                                                                              PRINTER
                                                                                                                                                                                                  PUSH AF
                                                                                                                                                                                                                                    ;FDA3H=64931
 FD18 FEOD
                                                                                                                                                                                                  PUSH AF
CALL PIO
                                                                                                                                                                                                                                    ;initialise PIO chip
```

CENTRONICS INTERFACE

```
FDAB DB1F
                                                 IN A, (P_A_DATA)
                                                IN A, (P_A_DATA)
BIT 0,A

JR NZ RDV
POP AF

DUT (P_B_DATA),A

LD A, (04H INMOXIDX

OUT (P_A_DATA),A istrobe sent
LD A, (04H INMOXIDX

OUT (P_A_DATA),A istrobe off
RET
                                                                                                                                                       FE31 04
FE32 04
FE33 78
FDAA CB47
FDAC 20FA
FDAE F1
FDAF D33F
FDB1 3E04
FDB3 D31F
FDB5 3E06
FDB7 D31F
FDB9 F1
FDBA C9
FDBA
FDAA CB47
                                                                                                                                                                                                         LD A,B
                                                                                                                                                        FE34 FE30
                                                                                                                                                       FE36 3BAE
                                                                                                                                                                                                         JR C COORDI
                                                                                                                                                       FE36 38AF
FE36
FE36 CD75FE
                                                                                                                                                                                                        ;All done now
CALL ESC_A
                                               LD A,12

CALL PRINTER

CALL CR LF

RET; ---> To ZX MON
FDBB 3FCF
                           PIG
FDBB 3ECF
FDBD D35F
FDBF 3EF9
FDC1 D35F
FDC3 3E0F
                                                                                                                                                                                                                                      ;FE44H=65092
                                                                                                                                                                                   STABLE
                                                                                                                                                                                                        PHEM BC
                                                                                                                                                       FE44 CS
FE45 00000000
FE49 00
FE4A 3E1B
FE4C CDA3FD
                                                OUT (P_A_DATA),A

| B lines=output |
| B lines=output |
| Strobe off |
| OUT (P_A_DATA),A
                                                                                                                                                                                                        DB 0,0,0,0,0
LD A,1BH
FDC5 D37F
FDC7 3E06
FDC9 D31F
FDC8 C9
                                                                                                                                                                                                        LD A,1BH
CALL PRINTER
LD A,"K" ;Graphic mode K
CALL PRINTER
LD A,000 ;0100H bytes being sent
CALL PRINTER
                                                                                                                                                       FE4F 3E4B
FE51 CDA3FD
FE54 3E00
FE56 CDA3FD
FE59 3E01
FE5B CDA3FD
                                                RET
FDCB
FDCC CDA3FD PRINTIT
FDCF FEOD
FDD1 CO
                                                CALL PRINTER
                                                                                         ;FDCCH=64972
                                                CP ODH
RET NZ : Code 201 (C9H) if linefeed
; is hardwired, else codeFDD1H=64977
; 192 (C0H)
                                                                                                                                                                                                        LD A,01H
CALL PRINTER
                                                                                                                                                       FE5B
FE5E 21005B
FDD1
FDD1
                                                                                                                                                                                                        LD HL, BUFFER
                                                PUSH AF
LD A,OAH
CALL PRINTER
POP AF
RET
                                                                                                                                                       FESE 210058
FE61 0600
FE63 7E
FE64 CDA3FD
FDD2 F5
FDD3 3E0A
FDD5 CDA3FD
                                                                                                                                                                                                        LD B,O
LD A, (HL)
CALL PRINTER
                                                                                                                                                                                   PLP
 FDD8 F1
                                                                                                                                                       FE67 23
                                                                                                                                                                                                         INC H
                                                                                                                                                       FE67 23
FE68 10F9
FE6A CD6FFE
FE6D C1
FE6E C9
FDD9 C9
                                                                                                                                                                                                        DJNZ PLP
CALL CR_LF
 FDD9
FDD9
FDD9
FDD9
                                                                                                                                                                                                         POP BC
                           : ROUTINE No 3 -- COPY
                                                                                                                                                                                                         RET
                                                                                                                                                       FE6E
FE6F 3E0D
                                                                                                                                                                                                        LD A,ODH
CALL PRINTIT
RET
FDD9
                           RANDOMIZE USR (COPY)
                                                                                                                                                                                   CR_LF
                                                                                                                                                       FE71 CDCCFD
FE74 C9
FE74
FDD9
                                               CALL ESC_A
LD A,8
CALL PRINTER
CALL CR_LF
FDDA CD75FE
                           CUBA
                                                                              ;FDDAH=64986
FDDD 3E08
FDDF CDA3FD
FDE2 CD6FFE
                                                                                                                                                       FE74
FE75 3E1B
FE77 CDA3FD
FE7A 3E41
FE7C CDA3FD
FE7F C9
                                                                                                                                                                                                        LD A,1BH
CALL PRINTER
LD A,"A"
CALL PRINTER
                                                                                                                                                                                   FSC A
FDE2
FDE5 0600
FDE7 0E00
FDE9 DD21005B
                                               LD B,O ;Row O
LD C,O ;Column O
LD IX,BUFFER
                           COORD1
                                                                                                                                                       FE7F
                                                                                                                                                       FE7F
FE7F
FE7F
                                                                                                                                                                                   ; ROUTINES 1 & 2 -- LLIST LPRINT
 FDE9
                                                 :Address conversion
                                                                        yUse B & C software counters;
to create a Spectrum;
display-file address in
;register HL
;00110000
FDED 79
                                               LD A,C
SRL A
LD L,A
LD A,B
                         COORD2
FDEE CB3F
FDFO &F
FDF1 78
                                                                                                                                                       FE7F
                                                                                                                                                       FEBO F5
                                                                                                                                                                                   NEWPRINT PUSH AF
                                                                                                                                                                                                                                           :FEB0H=A5152
 FDF2 E630
                                                                                                                                                       FEB1 FE7F
FEB3 2004
FEB5 3E63
FEB7 1846
                                                                                                                                                                                                        CP 127
JR NZ C4
LD A,"c"
JR EXIT
                                                AND 30H
FDF2 E630
FDF4 OF
FDF5 67
FDF6 78
FDF7 E60E
FDF9 07
FDFA 07
                                                                                                                                                                                                                                 ;Process copyright code
                                                RRCA
                                                RRCA
LD H,A
LD A,B
AND OEH
                                                                        :00001110
                                                                                                                                                                                                        DB 0,0,0
                                                                                                                                                       FE89 000000
                                                                                                                                                                                   C4
                                                                                                                                                                                                                                 Process codes 128-143
                                                                                                                                                                                                                                ;Process codes 128-143
;FE89H=65161
;C4 to hold JP CHUNKY <C3 xx xx>
;C5 to read CP 144 (90H) if printer
;is Epson MX80 with block graphics
                                                RLCA
                                                                                                                                                       FE89
                                                RLCA
RLCA
RLCA
                                                                                                                                                       FER9
FDFB 07
FDFC 07
FDFD B5
FDFE 6F
                                               OR L
LD L,A
LD A,B
AND 1
RLCA
                                                                                                                                                                                                       CP 128
JR C C6
CP 164+1
JR NC C6
DB 0,0,0
LD A,20H
JR EXIT
                                                                                                                                                       FEBC FEBO
                                                                                                                                                                                   C5
                                                                                                                                                                                                                                 ;Process codes xxx-164
                                                                                                                                                      FEBÉ 3808
FE90 FEAS
FE92 3007
FE94 000000
FDFF 78
FE00 E601
FE02 07
FE03 07
FE04 B4
                                                                                                                                                                                                                                or C3 xx xx <JP NUMBER> FE94H=65172 code 5D is 1
                                                RLCA
                                                DR H
DR 40H
                                                                                                                                                       FE97 3E20
                                                                                                                                                                                   C3
                                                                                                                                                      FE99 1834
FE99
FE9B FEFF
FE05 F640
                                                DR 40H
LD H,A
;HL valid for use in FORM
PUSH BC
FE07 67
FE07 67
FE08 C5
FE09 0608
                                                                                                                                                                                                       CP 255
JR Z RESW
CP 165
JR C EXIT
CP 255
JR NC EXIT
                                                                                                                                                                                   C6
                                                                                                                                                                                                                                :Process tokens
                                                                                                                                                      FE9D 2808
FE9F FEAS
                                               LD B.8
                                                                                                                                                      FEA1 382C
FEA3 FEFF
FEA5 302B
FEA7 F5
FE09
FEOB ES
                                               PUSH HI
                          FORM2
FEOC 1600
FEOE 3E08
FE10 4E
FE11 58
                                               LD D,O
LD A,B
LD C, (HL)
LD E,B
                                                                                                                                                                                  RESW
                                                                                                                                                                                                       PUSH AF
LD A,20H
CALL OUTBYTE
POP AF
LD HL,TAB1
                                                                                                                                                      FEAB 3E20
FEAB CDF3FC
FEAD F1
FEAE 219600
FEB1 D6A5
                                               SRL C
DEC E
JR NZ F3B
RL D
                           F3B
FE12 CB39
FE14 1D
FE14 1D
FE15 20FB
FE17 CB12
FE19 24
FE1A 3D
                                                                                                                                                                                                        SUB 165
JR Z R3
LD B,A
                                                                                                                                                      FEB3 2808
                                               RL D
INC H
DEC A
JR NZ F2B
LD A,D
POP HL
                                                                                                                                                      FEBS 2808
FEBS 47
FEB6 CB7E
FEB8 23
FEB9 28FB
                                                                                                                                                                                                       BIT 7, (HL)
FE1B 20F3
FE1B 20F3
FE1D 7A
FE1E E1
FE1E
FE1F DD7700
                                                                                                                                                                                                        JR Z R2
DJNZ R2
                                                                                                                                                      FEBB 10F9
                                                                                                                                                                                                       LD A, (HL)
BIT 7,A
JR NZ R4
                                                                                                                                                      FERD 7E
                                                                                                                                                                                  R3
                                               LD (IX+O),A
INC IX
DJNZ FORM2
                                                                                                                                                      FEBE CB7F
FECO 2006
FEC2 CDF3FC
FEC5 23
FE22 DD23
FE24 10E5
FE24
FE26 C1
                                                                                                                                                                                                        CALL OUTBYTE
                                                                                                                                                                                                       INC HL
JR R3
RES 7,A
CALL OUTBYTE
                                                                                                                                                      FEC6 18F5
                                               POP BC
                                                                                                                                                     FECO CODE
FECO CODE
FECO SEZO
FE26
FE26
                                               ;Do next column
FF24
                                                                                                                                                                                                       LD A.20H
                                                                                                                                                                                                                               ;trailing space
FE27 OC
FE28 OC
FE29 79
                                               INC C
                                                                                                                                                      FECD
                                                                                                                                                      FECF CDF3FC
                                                                                                                                                                                                      CALL DUTBYTE
POP AF
RET ;--->disappear back into ROM
                                                                                                                                                                                  EXIT
FE29 79
FE2A FE40
FE2C 388F
                                               LD A,C
CP 64
JR C COORD2
                                                                                                                                                     FED3 C9
FED3
                                               ;Send print buffer to printer
CALL SINGLE ;or Call Double FE2EH=65070
;Step down a Row
FE2C
                                                                                                                                                      FED4
                                                                                                                                                                                                      EQU #
EQU FIN-PRINTER ;0131H=00305
                                                                                                                                                                                  FIN
                                                                                                                                                                                  LEN
FE2E CD44FE
                                                                                                                                                      0131
FF2F
                                                                                                                                                     0131
```

PDV



FC00 2A 4F 5C 11 0F 00 19 44 4D 11 80 FE 73 23 72 CD FC10 BB 6F FE C9 3F 1B CD CC FD 3E 45 CD CC FD FC20 3F OD C3FF FC C5 CD 3A FC CD **7A** 6F FC CD FE CD FC30 3A FC CD 54 FC CD 6F FE C1 **C9** 3E CD 1 B A3 FD FC40 2A CD A3 FD 3E 04 CD **A3** 3E FD 00 CD A3 FD 3F 02 FC50 CD **A3** C9 06 00 21 00 5B 5E 23 CB CB 23 CB 23 FC60 CB 23 OF 04 23 CB F5 CB 12 F1 CB 12 OD 20 F5 FC70 CD A3 FD CD A3 FD 23 10 E0 C9 06 00 21 00 5B FC80 OE 04 F1 CB 23 F5 CB 12 CB 12 OD 20 F5 7A CD A3 FC90 FD CD 40 A3 FD 23 10 E8 **C9** 00 00 06 00 01 18 02 FCA0 06 00 2A 98 FC 22 2D FD 32 AF 2F FD 2A 94 FC ED FCBO 5B 2D 52 FD **B7** ED 28 25 EB BO 7E 23 22 2D FD 28 FCCO 17 F5 CB 3F CB 3F CB 3F CB 3F CD E3 FC F1 CD E3 FCDO FC 3F 20 CD FF FC 18 D4 CD FF FC 18 CF 3E OD CD FCEO FF FC **C9** E₆ OF FE OA 38 02 C6 07 C₆ 30 CD FF FC FCFO C9 00 46 4F 3A F1 FC **B7** 79 CA CC FD C3 FF FC E5 **3A** FD00 6F 3A F2 FC 67 2F 30 FD **3C** 32 2F FD BC 20 OE FD10 CD 6F FE AF 32 2F FD 7D FE OD 28 OF 18 E3 7D FF FD20 OD 20 05 AF 32 2F FD 7D CD CC FD E1 C9 00 00 00 FD30 00 26 00 6F 11 78 FD DD 21 6E FD 3E 2F DD 4F 00 FD40 DD 46 01 C6 01 ED 42 F2 43 FD 09 12 DD 23 DD 23 FD50 13 OD 20 E7 3E 5B CD F3 FC 3E 31 CD F3 FC 34 **7B** FD60 FD CD F3 FC 23 3A 7C FD CD F3 FC C3 97 FE 10 27 FD70 EB 03 64 00 01 OA 00 00 00 00 00 00 FE 00 80 DA FE FD80 8C FE FE 90 D2 80 16 00 5F 21 13 FD B7 19 7E FD90 C3 CF FE AO A2 A1 A3 **A8** AA A9 AB A4 A6 A5 A7 AC FDAO AE AD AF F5 FD F5 CD BB DB 1F CB 47 20 FA F1 D3 3E **FDBO** 3F 04 D3 1F D3 3E 06 1F F1 C9 3E CF D3 5F 3F FDCO F9 D3 3E OF D3 7F 3E 06 D3 1F **C9** CD A3 FD FE FDDO OD CO F5 3E 75 OA CD A3 FD F1 C9 CD FE 3E 08 CD FDEO A3 FD CD 6F 00 FF 06 00 OE DD 21 79 00 5B CB 3F FDFO 6F 78 30 OF 78 E6 OE 67 E₆ 07 07 07 **B5** AF 78 FE00 E6 01 07 07 **B4** F₆ 40 67 C5 06 08 **E5** 16 00 3E 08 FE10 4E 58 CB 39 1D 20 FB CB 12 24 3D 20 F3 7A F1 DD FE20 77 00 DD 23 10 E5 C1 OC OC. 79 FE 40 38 BF CD 44 FE30 FE 04 04 78 FF 30 38 AF CD 75 FE 3E OC CD A3 FE40 CD FE C9 C5 6F 00 00 00 00 00 3E 1B CD A3 FD 3F FE50 4B CD A3 FD 3E 00 CD A3 FD 3E 01 CD A3 FD 21 00 7E CD FE60 5B 06 00 A3 23 FD 10 F9 CD 6F FE C1 **C9** 3E FE70 OD CD CC FD **C9** 3E 1B CD A3 FD 3E 41 CD A3 FD **C9** FE80 F5 FE 7F 20 04 3E 63 18 46 00 00 00 38 FF 80 OB FE90 FE A5 30 07 00 00 00 3E 20 18 34 FE FF 28 08 FE 38 FEAO A5 2C FE FF 30 28 F5 3E 20 CD F3 FC 21 96 FEBO 00 D6 A5 28 08 47 CB 7E 23 28 FB 10 F9 7E CB 7F FECO 20 06 CD F3 FC 23 18 F5 CB BF CD F3 FC 3E 20 CD FEDO F3 FC F1 C9 00 00 00 00 00 00 00 00 00 00 00 00

Figure 1 HEX DUMP



;380Z TO SPECTRUM ONE-WAY LINK, TO CONNECT ;WITH THE PARALLEL PRINTER INTERFACE ;R SARGENT July 1983

P A DATA EQU 1FH

PRINTIT	EQU	OFD80H	:64896d	(48K	Spectrum)
	: Or	7D80H	321284	(1AK	Spertrum

PIO	EQU OFD6FH ;64879d
	;or 7D6FH 32111
	;Deduct 32768 to achieve addresses
	;for the 16K Spectrum

		;Don't forget to	adjust the CALLs
F3	L380Z	DI	;Interrupt off
CD6FFD		CALL PIO	;Set PIO
CD		CALL S_N_RDY	;Send "Not ready"
01FEF7		LD BC,63486	
ED78	WAIT_GO	IN A, (C)	;Start when
CB47		BIT O,A	;key "1" is
20FA		JR NZ WAIT_GO	;pressed
CD	MLOOP	CALL G_DATA	;Get 4 bits
CD		CALL S_N_RDY	;Not ready
4F		LD C,A	;because
CB39		SRL C	; the
CB39		SRL C	;bits
CB39		SRL C	;are being
CB39		SRL C	;shifted
3E80		LD A, BOH	
3D	DEL	DEC A	;Delay
20FD		JR NZ DEL	
CD		CALL G_DATA	;Next 4 bits
E6F0		AND OFOH	;merged with
B1		OR C	;first 4
CD		CALL S_N_RDY	;Not ready
CD80FD		CALL PRINTIT	;Print 8 bits
18DE		JR MLOOP	;go round again
3E02	G_DATA	LD A,2;00000010	;Send a
D31F		OUT (P_A_DATA),A	;"ready" signal
01FEEF		LD BC,61438	
DB1F	STR	IN A, (P_A_DATA)	;Read 5 bits until
57		LD D,A	
ED78		IN A, (C)	;Check if "O"
CB47		BIT O,A	;key pressed
2805		JR Z ÁBORT	The second secon
CB5A		BIT 3,D	:strobe received
20E3		TO NY CTO	

JR NZ STR

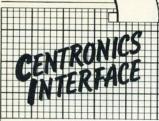
RET

;then exit with

;4 bits valid

C9

20F3



E1 ABORT POP HL
FB EI
C9 RET

F5 S_N_RDY PUSH AF ;Send a
3E06 LD A,6;00000110 ;"not ready"
D31F OUT (P_A_DATA),A ;signal
F1 POP AF

Program 2b

09

;380Z TRANSMIT TO SPECTRUM USING 7 BIT ;1/O PORT 8 BITS OF DATA SENT AS ;TWO SEPARATE NIBBLES ** PROVISIONAL **

RET

CENTRON EQU 0000; enter appropriate number

the number will be found contained; in locations FF25/FF26H at switch-

; adjust

;on time

ORG 1000H ;LOAD WHERE-EVER

F5 ENTRY PUSH AF F5 PUSH AF

CALL CENTRON ;Send low nibble

CALL CE
AF XOR A
3D DY DEC A

20FD DY DEC A ;Delay

F1 POP AF

IF RRA ;Shift
IF RRA ;high
IF RRA ;nibble
IF RRA ;and

CD____ CALL CENTRON ;send it
AF XOR A
3D DDY DEC A ;Delay

20FD JR NZ DDY F1 POP AF C9 RET

AF RELEASE XOR A ;Continue CD_____ CALL ENTRY ;sending

F71E DB EMT, KBDTC ; nulls to Spectrum
FE30 CP "O" ; untill stopped by
20F6 JR NZ RELEASE ; the "O" key

F700 DB EMT,0 ;Return to Command level



Increasing accuracy boosts Spectrum uses

Stephen Rush manipulates strings to perform mathematical functions

NE OF the problems with the Spectrum, and most other home computers when used for accountancy, astronomy and the like where numbers have to be very precise, is that the Spectrum will display numbers only to eight digits after which either it will ignore the rest of the number — if the number is between 1018 and 101-6 — or it follows the number with a letter E and the power of 10.

That unfortunately is often insufficient when performing very complex calculations. Also occasionally, if not very often, numbers bigger than 10138 or less than 10† – 38 are required and they not only cannot be held to any real accuracy but also cause the Spectrum to stop with an error report, leaving you to find another way of finishing the problem.

SLICING

The easiest way to circumvent those problems is to use string variables to hold the numbers — they will hold the numbers to any amount of places without the Spectrum trying to change them for you — and manipulate those strings, using the excellent string-slicing capabilities, to perform the four major mathematical operations.

To get down to the programming, please note that the first four subroutines are only to demonstrate the
basics and so to make them easier to
understand they will work only with
positive integers — i.e., no negative
numbers and no decimal points —
but the final program which contains
adapted versions of the programs also
contains other routines which allow
the user to enter both decimals and
negative numbers.

The most obvious and probably easiest program is a simple addition program which will add two strings and leave the answer in a third string. Note that if you enter the multiply program you will need to have this addition routine in the computer as it is an integral part of the multiply procedure.

If you look at figure one you will see that the routine is based on a simple loop which looks at successive elements of each string, adds them, and puts the answer in a third string. The main lines of interest are:

Line 1080, which sets the variable Z to the value of the '7'th element of the shorter string, after first checking that the string is long enough to allow it

DIVIDES

Line 1090 adds the 'f'th element of the longer string to Z, adds any remainder from the last run through the loop and then divides the answer by 10.

Figure 1. Add.

1000 REM ADD 1010 INPUT "First No.",F\$: INPUT "Second No.",S\$
1020 LET T\$= "": LET R = 0 1030 LET C\$ = "0" + F\$: LET D\$ = "0" + S\$ 1050 IF LEN S\$>LEN F\$ THEN LET C\$ = "0" + S\$: LET D\$ = "0" + F\$ 1060 FOR F=0 TO LEN C\$-1 1070 LET Z = 0 1080 IF IEN D\$≥F+1 THEN LET Z = VAL D\$ (LEN D\$-F) 1090 LET R = (VAL(LEN (S-F)+R+Z)/101100 LET T\$ = STR\$ ((R - INT R)*10) + T\$ 1110 LET R = INT R 1120 NEXT F 1140 PRINT TS

Line 1100 puts the decimal part — the part after the decimal point — of the number, which corresponds to the units, on to the total (T\$) as the next digit of the final answer.

Line 1110 removes the decimal part of the number, the units, thus leaving R as the remainder from the addition.

This routine, although not fast, will add two numbers each 50 digits long in less than 10 seconds, which should not cause too much distress even if you want to add two very large numbers.

The next routine will subtract one string from another, though as we are still dealing only in positive numbers this routine will find the difference between the two numbers. If you want the routine to do a proper subtraction, you should add a line 640 to read:

IF S\$>F\$ THEN LET T\$="-"+T\$ to the program — figure two.

If you look at the subtraction routine in figure two you will see that, like the addition routine, it is based on a short loop but unfortunately this one is a little more complicated, so I will make its explanation a little more detailed.

COMPARING

Lines 530 and 540 are a simple if rather clumsy way of making both strings the same length by adding "0"s to the beginning of the shorter string.

Lines 560 and 570 set A\$ to the larger number and B\$ to the smaller number.

Beginners should note that when comparing strings the computer does not compare the values of the strings but instead checks on the codes of the



first element of each string — or successive elements if the first elements are the same — and the string which begins with the character with the higher code — see appendix A of the Spectrum manual — is the higher string.

Fortunately as the codes of the numbers are stored in numerical order — i.e., the code of "2" is larger than the code of "1" — this has the same effect as comparing the values of the strings. Unfortunately, however, if the strings are of different lengths you may arrive at the incorrect answer.

For example, if the computer compares the two strings "9" and "800", the computer will give the answer that "9" is larger, which is not the correct answer in this instance. To prevent that "0"'s have been added to make the strings the same length. So the example changes to "009" and "800" which gives the answer required, i.e., that "800" is larger.

NEXT DIGIT

Line 600 checks if the subtraction is to need a carry from the next digit.

Line 610 does the subtraction — R is the 'carry' if it is needed and R1 is the carry from the last subtraction if there was one — and leaves the result as the next digit of the final answer.

Line 620 then sets R1 to the number which has to be carried forward

Figure 2. Subtract.

500 REM SUBTRACT 510 INPUT "First No.",F\$, "Second No.".S\$ 520 LET T\$ = "" 530 IF LEN F\$>LEN S\$ THEN LET S\$ = "0" + S\$: GOTO 530 540 IF LEN S\$>LEN F\$ THEN LET F\$="0"+F\$: GOTO 540 550 LET R1 = 0 560 LET A\$ = "0" + S\$: LET B\$ = "0" + F\$ 570 IF F\$>S\$ THEN LET A\$ = "0" + F\$: LET B\$ = "0" + S\$ 580 FOR F = LEN A\$ TO 1 STEP - 1 590 LET R = 0 600 IF (VAL B\$(F)+R1)>VAL A\$ (F) THEN LET R = 10 610 LET T\$ = STR\$ ((VAL AS(F) + R) - (VAL BS(F) + R1)) + TS620 LET R1 = INT (R/10) 630 NEXT F

for the next time round the loop.

The program will find the difference between two numbers each 50 digits long in about 10 seconds.

The next program will multiply two strings by long multiplication. Figure three will show that the program is based on two small loops, one inside the other. The outside loop goes through one of the strings, one element at a time, and the inside loop multiplies that number by each element of the second loop in turn. Note that to use the multiplication routine you must have the addition routine in the computer or the multiplication program will not work.

Line 140 sets the remainder (R) to zero and sets the subtotal (Z\$) to the empty string.

Line 160 multiplies the correct elements of the strings and adds any remainder from the last multiplication.

Line 170 puts the units part of the calculation on to Z\$ as the next digit of the subtotal.

Line 180 calculates the remainder to be carried forward and puts the remainder in the variable R.

Lines 200 and 210 add "0"s to the subtotal and then send the subtotal and the current total to be added using the addition routine.

PURPOSE

For those who do not see the reason for doing this, here is a small example of a long multiplication:

If you want to multiply 12,654	12,654
by 124,	124*
you first multiply 12,654 by 4	
giving	50,616
then you multiply 12,654 by 2 and	
add a "0"	253,080
then multiply it by 1 and add	
"00" giving	1,265,400
and then to get the final answer	
you	-
add these three subtotals giving	1,569,096

That is, in effect, the purpose of lines 200 and 210, as they add "0"s and then send the strings to be added. The only difference is that those lines add the subtotals after each is calculated rather than waiting to the end and then adding them all.

If you wish to multiply two numbers each 50 digits long, unfortuFigure 3. Multiply.

98 REM To use this program you must have the addition routine in the computer 100 REM MULTIPLY 110 INPUT "First No." F\$ "Second No " SS 120 LET T\$ = "" : LET B\$ = S\$: LET A\$ = "0" + F\$ 130 FOR G=LEN B\$ TO 1 STEP-1 140 LET R = 0 : LET Z\$ = " 150 FOR F = LEN A\$ TO 1 STEP-1 160 LET A = (VAL B\$(G)*VAL A\$ 170 LET Z\$ = STR\$(((A/10) - (INT(A/ 10)))*10) + 7\$ 180 LET R = INT (A/10) 190 NEXT F 200 FOR H=1 TO LEN B\$-G: LET Z\$ = Z\$ + "0" : NEXT H 210 LET F\$ = Z\$: LET S\$ = T\$: GOSUB 1020 220 NEXT G 250 PRINT T\$ 260 STOP 1130 RETURN

nately this routine will take almost $12\frac{1}{2}$ minutes to arrive at the answer because to do the calculation requires:

A loop of 1 to 50 multipyling each digit by one digit from the other string; a loop of up to 1 to 50 adding "0"s to the subtotal; a loop of between 1 to 50 and 1 to 100 to add the total and the subtotal.

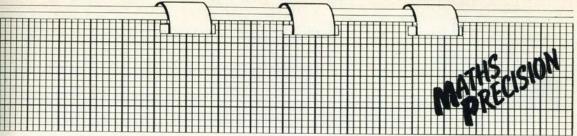
To complete the sum it will have to go through the process 50 times, which unfortunately takes a long time to complete. For many applications the answer is well worth the wait for the accuracy alone but you should also note that the biggest number the Spectrum can normally handle without stopping with an error is easily surpassed by multiplying two numbers each only 20 digits long.

DENOMINATOR

The next routine is slightly limited in that the number by which you are dividing —the denominator — may be only eight digits long and unfortunately that applies to the main program as well as the short routine. If anyone knows of a way of circumventing this, I would love to know how.

The number into which you are dividing — the numerator — can be as long as needed and you also have

650 PRINT TS



the option of choosing to how many places you want the answer worked.

What is probably a much bigger blow to people who will not bother to type-in the main program but want to see the division routine in action is that we are still dealing only in integers and consequently the answer will be a string of numbers and you will have to find where to put the decimal point — i.e., "10" divided by "40" will give the answer "0025" instead of "0.25".

If you look at figure four you will see that the routine is very short and the only lines of interest are those between 1565 and 1590.

DECIMAL

The easiest way to describe the tasks of those lines is to show an example of a long division next to a description of how the computer does the calculation. For example, if you wanted to divide 9,714 by 12 to five places, in practice you would PRINT 9714/12 but showing an example to 20 places would take too much room and would also put my one typing finger out of action for the rest of the week. The calculation would be:

Line	Action	The division
		08095
1510	Sets F\$ to "9714" and S\$ to	
	"12".	12)9714
	Sets Z to 9.	9
1580	Divides 9 by 12 and puts the answer (0) on the total.	
1590	Calculates the remainder (9).	
1565	'Brings the 7 down'.	97
1570	Sets R to the number formed (97).	
1580	Divides 97 by 12 and puts the	97_
	answer (8) on the total.	(12*8)
1590	Finds the remainder (1).	1
1565	'Brings the 1 down'.	- 11
1570	Sets R to the number formed (11).	
1580	Divides 11 by 12 and puts the	11_
	answer (0) on the total.	(12*0)
1590	Finds the remainder (11).	11
1565	'Brings the 4 down'.	114
1570	Sets R to the number formed (114).	
1580		
	the answer	114_
	(9) on the total.	(12*9)
1590	Finds the remainder (6).	6
1565	Finds that F\$ is too short so	
	it 'brings down a 0 down'.	60
1570	Sets R to the number formed (60).	

1580 Divides 60 by 12 and puts the answer (5) on the total.

1620 Arrives at the final answer 08095

As you can see, the decimal point still needs to be placed and the easiest way of doing it is to PRINT VAL F\$/VAL S\$, which will give an approximate value which should help you to place the point. This routine will do a division to 1,000 places in about three minutes.

As you have probably already said to yourselves, the routines are almost useless on their own as much of the calculations you will want to do involves negative numbers and/or numbers with decimal points.

To circumvent those problems requires two more routines. The first — lines 9500 to 9550 of the main program, figure five — searches the two strings — F\$ and S\$ — to find any decimal points. When it finds a decimal point it removes it and remembers from where in the string it came. The routine sets DP1 to the number of digits after the decimal point in F\$ and DP2 to the number of digits after the decimal point in S\$.

The other routine, from 9650 to line 9600, checks if the first character of F\$ or S\$ is a negative sign; the variable NEG is set to 0 if both numbers are positive, to 1 if the first is negative and the second positive, to 2 if the first is positive and the second negative, and to 3 if both numbers are negative.

CHECKING

The final routine, though not very important, is very useful in tidying

Figure 4. Divide. 1500 REM DIVIDE 1510 INPUT "First No.",F\$, "Second No.",S\$ 1520 INPUT "No. of places",P 1530 LET T\$ = "" 1550 LET R = 0 1560 FOR F = 1 TO P 1565 LET Z=0: IF LEN F\$>F THEN LET Z = VAL F\$ (F) 1570 LET R = R*10+Z 1580 LET T\$ = T\$ + STR\$ (INT(R/VAL S\$1) 1590 LET R = R - (INT(VAL T\$(LEN T\$))*VAL S\$) 1600 NEXT F 1620 PRINT T\$

the answers; its job is to remove the trailing and preceding zeros which make the answers look ugly. The important lines of this final routine are:

Line 9930 removes preceding zeros from the variable T\$.

Line 9920 returns if the number has been reduced to "0".

Line 9940 is a complicated line which does a fairly simple job; its task is to search the string for a decimal point; if it finds a point it continues with line 9950 but if the string does not contain a decimal point it will return, after first checking whether the string should be negative. This line prevents the rest of the routine removing zeros from a number greater than 1 as they do not need the zeros removing, e.g., you do not want the zeros removed from 9500.

Line 9965 will then replace a zero before the number if it starts with a decimal point — i.e., ".45" will be changed to "0.45". The results from the first two routines are used in the following ways:

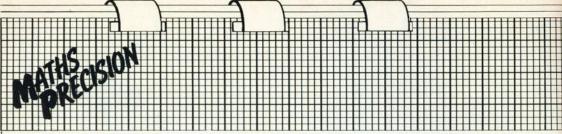
For adding, if the first number is positive and the second negative, line 7520 sends the strings to be subtracted. If, however, the first number is negative and the second positive, line 7530 swaps F\$ and S\$ and also swaps DP1 and DP2 before sending them to be subtracted.

Lines 7560 and 7565 ensure that both numbers have the same number of decimal places by adding "0"s to the string with fewer places after the point.

Line 7670 finds the position of the point in the final answer. When you add two numbers each with x digits after the point the answer will also have x digits after the point and will also make the answer negative if you were adding two negative numbers—i.e., if NEG 3.

To subtract, if the first number is positive and the second negative you are effectively adding two numbers, so line 8040 sends the strings to be added; similarly if the first number is negative and the second positive you are adding the two negative numbers, so line 8030 sends the strings to be added.

Lines 8050 and 8060 ensure that



both numbers have the same number of decimal places.

Line 8190 then finds the position of the decimal point, using the same reasoning that let line 7670 position the point for the add routine.

To multiply, line 8640 makes the final answer negative if one of the numbers was positive and the other negative. This line also places the decimal point for you - when you multiply a number with x digits after the point by a number with y digits after the point the answer will have x + y digits after the point.

To divide, line 9090 finds approximately where the decimal point will be in the final answer; the number of places before the point is roughly the difference in the numbers of places before the points in the original numhers

ANSWER

Line 9100 allows for people dividing by numbers less than 1.

Line 9120 checks if the second number is larger than the first, in which case the answer will have one fewer digit before the point.

Finally line 9130 works out the final answer miraculously.

When you have typed-in the program you will see that the lines from 100 to 7000 have been left blank and, as you may have guessed, that is where you put your problems. They should take the form LET F\$ = "First No.": LET S\$ = "Second No." followed by one of the following:

GOSUB ADD GOSUB SUBTRACT GOSUB MULTIPLY GOSUB DIVIDE

depending on how you wish the strings to be manipulated.

Also, if you require them, you have 17 memories at your disposal — all of the string variables except those used in the program. Do not use A\$, B\$, C\$, D\$, F\$, R\$, S\$, T\$, or Z\$. For example, LET M\$=T\$ will put the result of the last calculation in memory M\$. Before using those memories, however, you should check that you need to store the number, because remembering very large numbers will use up a great deal of your

precious RAM — the program takes about 3.1K and another 300 bytes are used by the minimum variables necessary for the program to run. That, of course, will increase considerably when you have to add your own lines.

DO NOT RUN

If, however, you are unfortunate enough to get error 4 OUT OF MEM-ORY then do not use RUN or CLEAR as they will erase the program variables. Instead you should do one of the following:

Look through the routine you are using and see which string variables are not used and set them to the empty string - e.g., Z\$ is not used in the ADD routine, so if you run out of memory while using the routine LET Z\$="" followed by CONTINUE may help.

The following lines may be removed as they are not important to the running of the program -7500, 8000, 8500, 9000, 9500, 9650, 9900, and also line 2 if you do not mind sacrificing my vanity.

If you are still short of memory the only solution is to start cannibalising the routines you are not using and then reLOAD the program before using it again.

The main use of the program will be for normal +, -, *, /, and here is an example which will divide 97402.000055 by 98.64 and will then add -65 - to do this sum you should add the following lines to the main problem:

100 LET F\$ = "97402.000055"

110 LET S\$ = "98.64"

120 GOSUB DIVIDE

130 LET F\$ = T\$

140 LET S\$ = "-65"

150 GOSUB ADD

Another use of the program is to find high powers of numbers by multiplying repeatedly by the same number. For example, to find the exact value of 17.64 to the power of 35, lines 100 to 150 from above should be removed and the following lines should be added:

100 LET F\$="17.64" 110 FOR Y = 2 TO 35 120 LET S\$ = "17.64" 130 GOSUB MULTIPLY 140 LET F\$ = T\$ 150 NEXT Y

Once this program is running satisfactorily I suggest you go for a stroll to the local shops and buy a paper and a box of tea bags as the program is very slow to reach the exact answer.

Remember when using FOR NEXT loops that you must not use the variables used in the main program as the loop counter — i.e., do not use P, F, Z, R, G, A, or H as the loop counter.

Another function which can be performed easily using this program, but is not practical without it, is a factorial routine. The factorial of a number can be found by multiplying all the numbers between 1 and the number together, i.e., 6 factorial 6*5*4*1*3*2*1)

SMALL ROUTINE

To find the factorials of 100, 69, and 30 the following lines should be added to the main program:

100 LET F\$ = "2"

110 FOR Y = 3 to 100

120 LET S\$ = STR\$ Y 130 GOSUB MULTIPLY

140 LET F\$ = T\$

150 IF Y = 30 THEN PRINT "30 Factorial is "; T\$

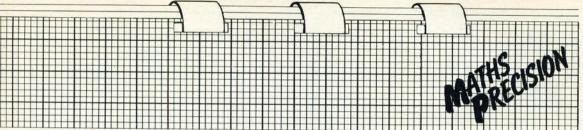
160 IF Y=69 THEN PRINT "69

Factorial is "; T\$ 170 NEXT Y

180 PRINT "100 Factorial is";

Before finally leaving Spectrum owners I would like you all to try to write a small routine - the ones you write are always the best - which will round the last digit of the total - i.e., it will round 0.6666 up to 0.667 and will round 8.3422 down to 8.342 which may make the final answer more meaningful. Without the routine 10/3*3 will give the answer 9.999 but with the routine the 9.999 will be rounded-up to the proper 10.

If you have not already done so, try to write this routine on a piece of paper - or on the computer if you find it easier to concentrate when sitting in front of the keyboard. If your routine works it will show that you understand at least the basics of the program and should be able to adapt the routine if your particular problem needs the program altering



in some way. I suppose I had better show one routine which will do the job for those who could not bother and for those who could not get their routines to work properly:

11 LET ROUND = 7450 : LET ROU = 07450 REM ROUND 7460 LET F\$ = T\$: LET S\$ = "5" : GOSUB DP 7465 LET DP2=DP1: LET NEG = NEG*3: LET ROU = 1: GOSUB 7550 7470 IF T\$ (LEN T\$)="." THEN LET T\$ = T\$ (TO LEN T\$ – 1) 7475 LET T\$ (LEN T\$) = "0" 7480 GOSUB ZR 7490 PRINT "The answer after rounding is", T\$ 7675 IF ROU=1 THEN LET ROU = 0: RETURN

And using GOTO ROUND will round the number in T\$.

The program will go almost

straight on to a ZX-81 with only a few minor changes, due to the multi-statement lines on the Spectrum. Many of the lines can just be split into many separate lines, one after another. The only lines which will require any thought are those which contain IF THEN statements.

When translating them you must remember that if the statement is false — e.g., if NEG < >2 for line 7520 — then the rest of the line is ignored. Most of the lines with an IF THEN statement, and also more than one statement, will therefore require a GOSUB and RETURN or a GOTO. An example is line 7560 which could be replaced by:

7560 IF DP1>DP2 THEN GOTO 9700 9700 LET S\$= S\$ + "0"

9700 LET S\$ = S\$ + "0" 9701 LET DP2 = DP2 + 1 9702 GOTO 7650

This approach should help with all of the problem lines except line 9940,

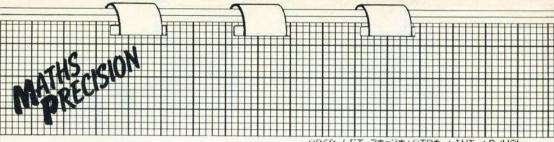
which must be replaced by:
9940 FOR F=1 TO LEN T\$
9941 IF T\$ (F)="." THEN GOTO
9950
9942 NEXT F
9943 LET T\$=("_" and
NEG=5)+T\$
9944 RETURN

You should be able to convert the rest of the program using those hints.

If you are still a ZX-80 owner, you will probably have to try to think how the routines work and try to adapt them, remembering that the ZX-80 does numbers only as integers — the strings will still hold decimal numbers but the problem is getting the correct numbers into the strings in the proper order. If anyone wishes to try to adapt the program I wish them the best of luck but from what I remember of the ZX-80 it is probably easier for you to write your own program, using any ideas from my program which help you with the task.

) REM PRECISION 2 REM S. RUSH 1983 9 INPUT "NO. OF PLACES (FOR D IVISIONO", P 10 LET T#="" LET ADD=7500 LE T SUBTRACT=8000: LET MULTIPLY=85 00 LET DIVIDE=9000: LET DP=9500 LET 2R=9900 15 REM the Problems should go between lines 20 and 7000 20 LET f\$="2675": LET \$\$="12": GU SUB divide 25 PRINT t# 30 LET f==t#: LET s#="-98": GO SUB add: PRINT to: LET forto: L ET s#="-16": GU SUB subtract: PR INT to: LET forto: LET so="72": GO SUB multiply: PRINT to: LET f \$=t\$: LET s\$="4": GO SUB divide 7400 PRINT "The answer is"; T\$ 7499 STOP 7500 REM ADD 7510 GO SUB DP 7520 IF NEG=2 THEN LET NEG=0: G O TO 8020 7530 IF NEG=1 THEN LET R#=F#: L ET F#=S#: LET S#=R#: LET DP3=DP1 LET DP1=DP2: LET DP2=DP3: LET

NEG=0: GO TO 8020 7540 LET MULT=0 7550 LET TO="": LET R=0 7560 IF DP1>DP2 THEN LET S#=S#+ "0": LET DP2=DP2+1: GO TO 7560 7565 IF DP2>DP1 THEN LET F#=F#+ "0": LET DP1=DP1+1: GO TO 7565 7570 LET Ca="0"+Sa: LET Da="0"+F 7575 IF LEN FEDLEN SE THEN LET C\$="0"+F\$: LET D\$="0"+S\$ 7580 FOR F=0 TO LEN C#-1 7590 LET Z=0 7600 IF LEN D\$>F+1 THEN LET Z=V AL DE(LEN DE-F) 7610 LET R=(VAL (C\$(LEN C\$-F))+R +2)/10 7620 LET T\$=STR\$ ((R-INT R)*10)+ TT 7630 LET R=INT R 7640 NEXT F 7650 IF MULT=1 THEN LET MULT-U: RETURN 7670 LET T#=("-" AND NEG=3)+T#C TO LEN TS-DP1)+"."+TS(LEN TS-DP1 +1 TO) 7600 GO TO ZR 8000 REM SUBTRACT



8010 GO SUB DE 8020 LET T#="" 8030 IF NEG=1 THEN LET NEG=3: G 0 TO 7540 8040 IF NEG=2 THEN LET NEG=0: G 0 TO 7540 F#="0"+F#: GO TO 8080 8090 LET R1=0 8110 IF F#>S# THEN LET A#="0"+F #: LET B#="0"+S#: GO TO 8130 8120 LET A\$="0"+S\$: LET B\$="0"+F #: LET NEG=3-NEG 8130 FOR F=LEN A\$ TO 1 STEP -1 8140 LET R=0 8150 IF (VAL B\$(F)+R1)>VAL A\$(F) THEN LET R=10 8160 LET T#=STR# ((VAL A#(F)+R)-(VAL B#(F)+R1))+T# 8170 LET R1=INT (R/10) 8180 NEXT F 8190 LET T#=("-" AND NEG=3)+T#(TO LEN Ta-DP1)+". "+Ta(LEN Ta-DP1 +1 TO) 8200 GO TO ZR 8500 REM MULTIPLY 8510 GO SUB DP: LET T\$="": LET B #="00"+5#: LET A#="00"+F# 9550 NEXT F

8530 FOR G=LEN B# TO 1 STEP -1 9560 REM NEGATIVE

8535 LET R=0: LET Z#="" 9570 LET NEG=0

8540 FOR F=LEN A# TO 1 STEP -1 9580 IF CODE F#=CODE "-" THEN L

8550 LET A=(VAL B#(G)*VAL A#(F)) ET NEG=1: LET F#=F#(2 TO)

4R 9590 IF CODE 9#=CODE "-" THEN L 8570 LET 2#=STR# (((A/10)-(INT (A/10)))*10)+2\$ 8580 LET R=INT (A/10) 8590 NEXT F 8600 FOR H=1 TO LEN BS-G: LET ZS =2\$+"0": NEXT H 8610 LET F#=2#: LET S#=T#: LET T 6="": LET R=0 8620 LET MULT=1: GO SUB 7570 8638 NEXT G "+T\$(LEN T\$-DP1-DP2+1 TO) 8650 GU TU ZR 9000 REM DIVIDE 9010 GU SUB DP: LET Z#="" 9030 LET R=0 9040 FOR F=1 TO P 9045 LET 2=0: 1F LEN F\$>=F THEN LET Z=VAL F#(F) 9050 LET R=R*10+Z

9060 LET Z#=Z#+STR# (INT (R/VAL ((事) 9070 LET R=R-(INT (VAL Z\$(LEN Z\$))*VAL S#) 9080 NEXT F 9085 PRINT 25 9090 LET DP3=(LEN F4-DP1)-(LEN S D: NEXT F 9120 IF S\$>F\$ THEN LET DP3=DP3-1 9125 IF DP3<1 THEN LET Z#="0"+Z #: LET DP3=DP3+1: GO TO 9125 9130 LET T#=("-" AND (NEG=1 OR N EG=2))+Z#(TO DP3)+"."+Z#(DP3+1 TO) 9140 GO TO ZR 9500 REM DP 9510 LET DP1=0: LET DP2=0 9520 FOR F=LEN F\$ TO 1 STEP -1: IF F#(F)="." THEN LET DP1=(LEN F#-F): LET F#=F#(TO F-1)+F#(F+1 TO > 9530 NEXT F 9540 FOR F=LEN S\$ TO 1 STEP -1: IF S#(F)="." THEN LET DP2=(LEN S\$-F): LET S\$=S\$(TU F-1)+S\$(F+1 TO > ET NEG=NEG+2: LET St=St(2 TO) 9600 RETURN 9900 REM ZR 9910 IF CODE T#=CODE "-" THEN L ET NEG=5: LET T#=T#(2 TO) 9920 IF T#="0" OR T#="0." THEN RETURN 9930 IF CODE T#=CODE "0" THEN . L ET T\$=T\$(2 TO): GO TO 9920 9940 FOR F=1 TO LEN T#: IF T#(F) 9950 IF T\$=".0" THEN LET T\$="0" RETURN 9960 IF T#<LEN T#>="0" THEN LET T#=T#(TO LEN T#-1): GO TO 9950 9965 IF CODE T#=CODE "." THEN L ET T#="0"+T# 9978 IF NEG=5 THEN LET T\$="-"+T 9980 RETURN

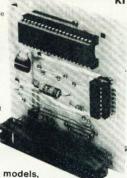
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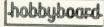


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Cheap arm demonstrates robot capabilities

Robots are great fun but they are expensive. Those used by British Leyland cost tens of thousands of pounds while many cheaper designs aimed at schools, colleges and the enthusiast cost anything between £700 and £1,000. Those prices are clearly outside the range of the average pocket. The robot described here aims to rectify that situation for £25 to £30; excluding the ZX-81 and an I/O board, a small and simple robotic arm can be constructed.

Although this robot has no practical use, it serves as an excellent demonstration tool or it can be built for pleasure. The materials required, apart from a ZX-81 and an I/O board, are two Acom radio control servos, some scraps of thin plywood or card, a suitable box and some old Meccano, or similar, rods and wheels.

The construction method need not be adhered to and the robot should be adapted to suit the individual and his materials. First the arm is constructed. The pattern of the arm — figure one — is copied on to ½in. plywood or thick card. The arm is cut out and folded along the dashed lines into a box shape. That is then taped or glued. Once the arm is complete, two holes are made as indicated in figure one. The holes are made using either

Stephen Crawford shows how by using existing materials and a ZX-81 you can obtain an idea of how large industrial concerns could develop in the years to come

a knitting needle or a special punching tool.

The next step involves making a platform on to which the arm and servo motors are attached. The platform — figure two — is cut from a piece of ½61n. plywood. Two Meccano brackets — figure three — are then bolted on to the platform as indicated in figure two. If Meccano is unavailable, similar items can be made from thin aluminium.

A lin. Meccano bush wheel is placed inside the arm so that the centre of the wheel is aligned with the two holes in the arm. The whole arm assembly is placed between the brackets and a 1½in. Meccano rod inserted through the holes. The bush wheel is

then glued to the arm and the grubscrew in the bush is tightened, fixing the arm and rod together solidly figure four.

The next stage involves the mounting of the servo. A gearwheel is fixed on to the right end of the rod, looking from behind. The purpose of the wheel is to provide a large surface area on to which a servo head may be attached. The servo is then mounted on to the platform. The centre of the servo head must be aligned accurately with the centre of the rod. To do that the servo must be raised to the correct height.

Small blocks of wood glued to the platform act as supports. The servo is attached, both to the gearwheel and the platform, by double-sided tape. It should be noted that the bolt securing the servo head to the servo should be removed, as it causes a bump which weakens the joint — figure five.

Once the robot part has been made the next stage is construction of a base. That is not critical and therefore detailed instruction has been omitted. Briefly, however, it can be made from wood or from a plastic or metal projects box. A diagram of minimum sizes is shown in figure six.

A suitable hole is cut in the top of the box to accommodate the servo. Once the hole has been cut, the servo is screwed into place. The platform

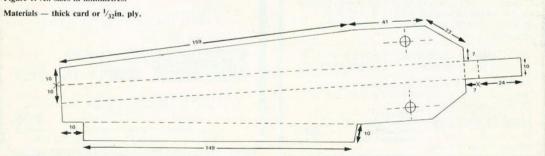
MATERIALS

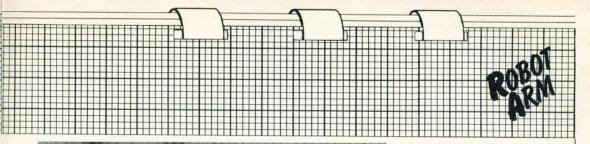
The servos — Acom AS-1 — used cost between £12 and £15. They may be obtained from any model shop which supplies Acom radio control equipment.

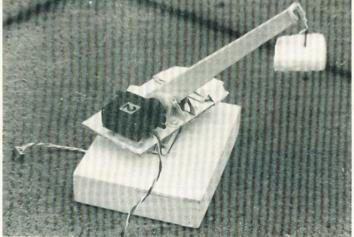
Since the number of stockists is enormous, the best way of finding a supplier of the servos would be to consult advertisements in an appropriate magazine.

The I/O board was obtained from Technomatic Ltd and cost £11.50 plus VAT.

Figure 1. All sizes in millimetres.







can then be attached to the base. To do that a servo head is either taped — double-sided — or glued to the underside of the platform — figure two.

The platform should then be balanced. The robot arm is fully outstretched and small weights are taped on to the platform in appropriate positions so that the platform is balanced about the centre of the servo head. The servo head attached to the platform is then connected to the servo on the base.

The final step involves constructing a hook for the end of the robot arm. That is made from a paper clip bent into the correct shape. The completed hook is glued to the end of the arm. The robot is then complete and it is ready to be connected to the computer.

So that the robot may be controlled, it must be linked to the computer via an interface board. In the

Figure 3. Meccano part 12B (2 required).

Elevation

prototype a Technomatic I/O board was used. It consists of eight inputs and eight outputs but only two outputs are required. Other output boards may be used but the machine code routine — figure nine — must be altered as in figure nine. The servos are connected to each line as shown in figure seven. Once that is complete the robot is ready to be programmed.

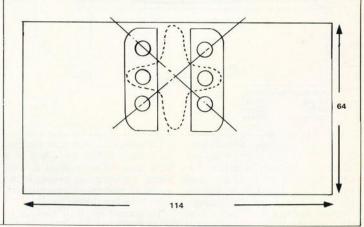
The radio control servos are controlled by changing the input pulse width between $1,000\mu S$ and $2,000\mu S$. For simplicity there are 100 programmable positions, i.e., for every $10\mu S$ the input pulse is increased the servo

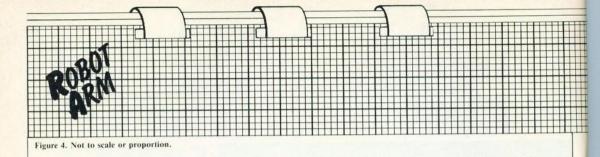
will turn about one degree. The time between each pulse is of the order of $18,000\mu S$. Figure eight illustrates the waveform required. The servos in the robot are controlled by a Z-80 machine code routine which is controlled by a Basic program. The machine code routine can be broken into three parts — the $18,000\mu S$ delay, the $1,000\mu S$ pulse, and the adjustable part of the pulse, lasting 0- $1,000\mu S$. The machine code routine and accompanying comments are in figure nine.

Unless you have an assembler the machine code will have to be loaded into the computer in decimal, using the loader program — program one. To enter the code the loader program is entered into the computer. The program is RUN and the decimal values — under the column Decimal — from figure nine are entered one at a time, pressing NEWLINE after each value.

Once all the values have been entered, press STOP then NEWLINE. That will terminate the loader program which is then deleted except for the REM statement in line one which will have taken a different appearance. Great care must be taken when entering the machine code, as one mistake can crash the program. If that happens the plug must be removed and the loading procedure repeated.

Figure 2. Pattern for platform showing position of the Meccano brackets and the servo head.





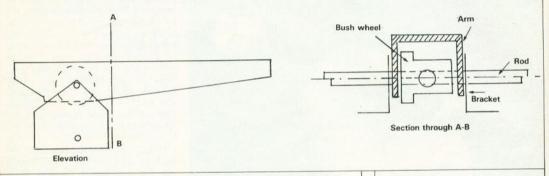


Figure 5. Not to scale or proportion.

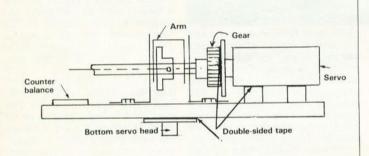
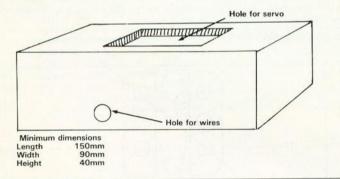


Figure 6. Not to scale or proportion.



The Basic programs controlling the machine code can then be entered. There are two such programs. The first — program two — is for manual control. In it the position of the servo motor is entered and the computer responds immediately. Program three

is for automatic control. In this case the servo positions are stored in a Basic array. The program is used where repetitive movement is required — perhaps to demonstrate a production line robotic arm.

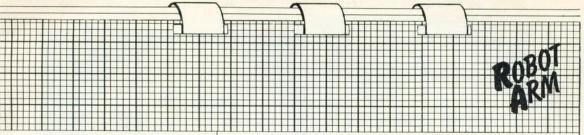
To operate the first, lines 20 to

Program 1: Machine code loader

1000 are entered. Lines 240 and 250 may be altered to suit the design of the robot. They have been included in case the robot has a limit to its movement, in which case the servo could pull the robot apart.

When the program is RUN a small menu is displayed. It indicates what code has to be entered for a certain movement. A number is entered which corresponds to a command, e.g., entering I means that any movement will be in the vertical axis — the arm will move. The computer then displays a prompt for the absolute position. An integer between I and 99 is entered and the computer will move immediately to the required position. Entering 50 centres the servo.

The second program operates by poking the required data into the machine code routine from a Basic array. Each movement requires two pieces of data — the axis of movement and the absolute position. The first piece of information is the axis of movement — 1 = horizontal (turntable); 2 = vertical (arm). The absolute position is an integer between 1 and 99.



Program 2: Manual control.

20 FAST 30 PRINT " ENTER NUMBER CORRESPONDING TO REQUIRED MOVEMENT. 1 = VERTICAL 3 = HORIZONTAL." 40 INPUT I 50 PRINT I 80 IF I = 3 THEN POKE 16536,1 90 IF I=1 THEN POKE 16536,2 220 PRINT "ENTER ABSOLUTE POSITION." 230 INPUT O 240 IF I = 3 AND Q > 90 OR Q < 1 THEN GOTO 220 250 IF I = 1 and Q > 75 OR Q < 5 THEN GOTO 220 260 POKE 16518,Q 270 LET L = USR 16515 1000 GOTO 20

Lines 30 and 50 have to be changed slightly if additional steps are required. For every one movement, the array is increased in size by two. Hence the DIM statement should also be increased by two. Line 40 should also be altered similarly. It should read 40 FOR F = 1 TO X STEP 2;X is increased by two for every extra movement.

The situation becomes clearer if the program is studied. In the demonstration program the data in the array should, when RUN, make the robot arm move almost fully up; rotate right; lower the arm; pause; raise the arm; rotate left; lower the arm and pause. That sequence will continue until the robot is stopped by halting the program.

On the ZX-81 all the programs have to be RUN in FAST mode. The reason is that when the computer is in SLOW mode the program is interrupted many times each second so that the display may be updated. In that case the critical timing required may be upset.

The servo routine and programs are not limited to the robot described. The same servos and software may also be used in other projects where critical and controlled movement is required.

The robot will not weld cars but, in addition to being an interesting toy, should demonstrate what industrial robots do.

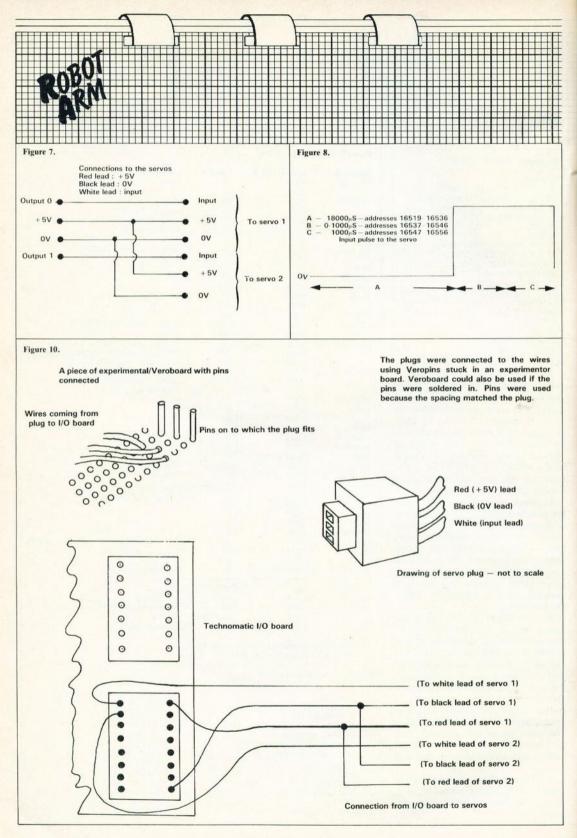
Figure 9: Machine code listing.

Mnemonic	Address (ZX-81)	Decimal	Comments
LD C,50	16515	14	Load C with 50 for 50 pulses.
	16516	50	
LD E,50	15617	30	Load E with value for servo
	16518	50	position: $1 \le E \le 99$.
D HL,3144	16519	33	Load HL with 3144 for about
	15620	72	18 000μS delay.
	16521	12	
D A,0	16522	62	Load A with 0 for compare
	16523	0	purposes.
NOP	16524	0	
DEC HL	16525	43	
CPH	16526	188	
RNZ	16527	32	Jump to address 16525 if
	16528	252	H ≠ A.
CPL	16529	189	
RNZ	16530	32	Jump to address 16525 if
	16531	249	L ≠ A.
LD HL,**	16532	33	Load HL with output port address.
	16533	248	Change values if another output port
	16534	42	having another address is used.
D (HL),1	16535	54	Set bit 0 of port high (this
	16536	1	can be changed depending on
			the serve being used).
DEC E	16537	29	
CPE	16538	187	
NOP	16539	0	
NOP	16540	0	
NOP	16541	0	
NOP	16542	0	
NOP	16543	0	
IRNZ	16544	32	Jump to address 16537 if
	16545	247	$E \neq A$.
NOP	16546	0	The state of the s
LD B,181	16547	6	Load B for delay C (Fig. 3).
	16548	181	2000 3 101 Well) C (1 Ig. 7).
NOP	16549	0	
NOP	16550	0	
DEC B	16551	5	
CPB	16552	184	
IRNZ	16553	32	Jump to address 16551 if
151.42	16554	252	B # A.
D (HL),0	16555	54	Set output port low
CD (TL),0	16556	0	Set output port low
DEC C	16557	13	
CPC	16558	185	
IRNZ		32	Ivers to address 16517 if
IKINZ	16559	100000	Jump to address 16517 if $C \neq A$.
DET	16560	212	
RET	16561	201	Return to Basic.

^{** 11000} for Technomatic I/O Port — 248 42 36850 for Latch Card — 242 143

Program 3: Automatic control.

20 FAST	1007 LET L(5) = 1
30 DIM L(16)	1008 LET L(6) = 10
40 GOSUB 1000	1010 LET L(7) = I
50 FOR F=1 TO 16 STEP 2	1011 LET L(8) = 10
60 POKE 16536,L(F)	1013 LET L(9) = 1
70 POKE 16518,L(F+1)	1014 LET L(10) = 50
90 LET L = USR 16515	1016 LET L(11) = 2
100 NEXT F	1017 LET L(12) = 90
110 GOTO 50	1019 LET L(13) = 1
1000 LET L(1) = 1	1020 LET L(14) = 10
1002 LET L(2) = 90	1022 LET L(15) = 1
1004 LET L(3) = 2	1023 LET L(16) = 10
1005 LET L(4) = 1	6000 RETURN



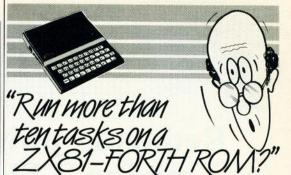


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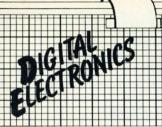
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Reducing power demand by mixing TTL and CMOS

Joe Pritchard continues his series on electronic theory, looking at interfacing different types of devices, buffers and other interesting areas

THIS TIME we will look at the interfacing of TTL and CMOS devices, at buffers, three-state devices and the computer databus. Why is it necessary to mix TTL and CMOS devices in circuitry? There are several reasons. One of them is that the CMOS devices load whatever they are connected to by only a small amount — less than that for an LSTTL device. They also consume less power. Also there are some functions available in the CMOS family which are not available in the TTL family.

CMOS units can also drive more CMOS inputs than can LSTTL units drive LSTTL inputs. CMOS devices will function on 5 volts, like TTL, but they will work satisfactorily up to 15 volts. Thus we might make an address decoder circuit from CMOS devices to minimise the loading effect on the computer bus and then interface it to LSTTL devices for the rest of the circuit.

Table one shows the input and output voltage characteristics of CMOS and LSTTL devices. They are typical values for the devices and so in practice some variation from the figures is to be expected. Let us consider the case of interfacing a CMOS device to an LSTTL input. The following con-

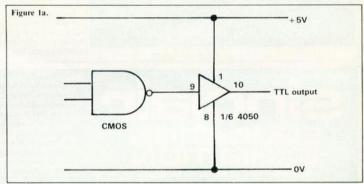
ditions must be met:

- i V_{OH} for CMOS must exceed V_{1H} for TTL
- ii V_{OL} for CMOS must be less than V_{II} for TTL
- iii The CMOS gate must be capable of handling the current needed by the TTL gate to switch correctly.

It is only in the last instance that we

fan-out to LSTTL of 1 at best. To produce a successful interfacing of signals, we employ a logic device known as a buffer. The usual device is the 4050, a CMOS buffer with the ability to supply the current needed.

The typical methods of use for the device are shown in figure one. Figure 1a shows the most common arrangement, with the power supply voltages



need to take care. The TTL input needs about 0.3mA when a low input is applied and the CMOS output is not capable of providing that current. The device may be able to provide the current but it is not guaranteed and so we do not want to rely on it.

The CMOS gate has, therefore, a

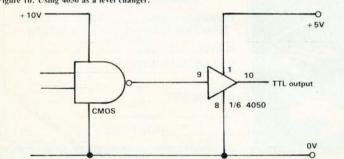
for the TTL and CMOS parts of the circuit being at the same level.

That need not be the case and in situations where the CMOS supply is higher than the TTL supply, the circuit configuration of figure 1b is used.

In most logic devices, applying a 1 signal with a value in volts of more than the supply rail will damage the device. That is not so with the 4050. In that application, we say that the 4050 is involved in a level conversion role — i.e., translating a voltage representing a logic 1 in one system into a voltage representing a 1 in another system. When used as a CMOS-to-TTL converter, the buffer can provide sufficient current for two LSTTL gates. The pin-out for the device is shown in figure two.

In the reverse situation, where we wish to interface a TTL output to a

Figure 1b. Using 4050 as a level changer.



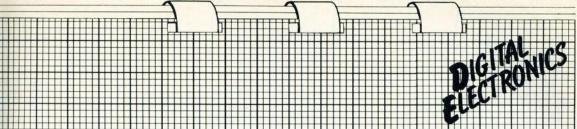


Table 1. Characteristics at 5V power supply.

Name	Description	CMOS (typical)	TTL (typical)
V _{OH}	Output high voltage	5V	3.4V
VOL	Output low voltage	0.01V	0.5V
V _{IH}	Voltage for high input	3.5V	2V
VIL	Voltage for low input	1.5V	0.8V

CMOS input, the situation is easier. We have no current considerations, due to the very low requirements of the CMOS devices. So all we have to consider are the voltage levels. When high, an LSTTL output is guaranteed to deliver about 2.4 volts. The minimum voltage a CMOS gate will recognise as a high input is between 3 and 4 volts.

They are the worst possible cases but we must design our circuits with

as having the ability to buffer a circuit, they also perform a logical invert function. They are called inverting buffers.

Some typical buffers are the 74LS16 and the 74LS17 devices. The 16 device is a hex inverting buffer and the 17 is a non-inverting device. The 4049 is a CMOS hex inverting buffer. As well as providing an interfacing function, we can use a TTL buffer to increase the fan-out of other LSTTL devices, e.g., feeding a standard TTL buffer from an LSTTL output and then taking the buffer output to other LSTTL devices.

A final use of buffers is in the field of protecting delicate chips from human beings. If we are using an advanced chip such as a parallel input/output device, known commonly as a PIO, then in experimental work or in education it is a good idea to input signals to the PIO only through buff-

current and voltage ratings of the two output types. The open collector outputs are capable of handling more current, typically up to 40mA. The 7407 and the 7406 are open collector driver devices; the phrase driver is used rather than a buffer because those devices can handle more current. Applications are shown in figure four.

The 7406 device has an inverting function and the 7407 a non-inverting function. In both cases, current flows through the load and into the TTL output when the output is low.

It is not only buffers which are available with the open collector outputs; other logic devices have them as well. A typical example is the 7401, which is the open collector equivalent of the 7400 device. An interesting side-effect of the open collector device is that it enables you to construct some logic functions without using logic gates. An example is the wired AND gate shown in figure five and its practical form in figure six. The totem pole output devices make this practice unwise but it is easily and safely implemented on the open collector devices.

Readers wanting an explanation

Figure 3. TTL-to-CMOS interface.

+ 5V

CMOS gate

them in mind. The answer is to use a simple resistor as a pull-up component. Its function is to ensure that whenever the TTL output goes high, the CMOS input always sees a minimum of 3.5 volts. That is shown in figure three. Obviously the resistor should be of a value so that when the TTL output is low, the CMOS input is low as well.

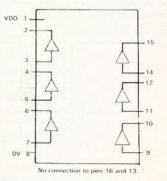
We have already seen the use of a buffer device in increasing the current availability from a logic device. Also we used a 7404 device in part one of the series to help us drive a LED. We could easily have used a buffer. Buffers can be obtained in which, as well

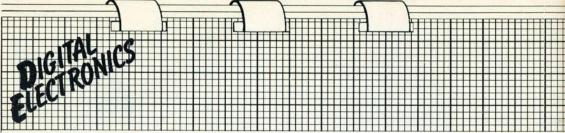
ers. In that way, if a high voltage was applied accidentally the buffers would be damaged and not the more expensive PIO.

All the buffers we have mentioned so far and which have belonged to the TTL family have had what are known as totem pole outputs. That rather graphic description refers to the internal design of the chips. There are some buffers which have a different type of internal circuitry at their outputs, while still being members of the TTL family.

Those devices are said to have open collector outputs. The main difference so far as we are concerned is the

Figure 2. Pin-out for 4050.





are advised to seek a reference book on the subject, as to explain it here would involve a rather detailed examination of the internal design of the TTL family of devices. Suffice to say that the 'wired AND' function is just one of a series of wired logic functions which can be formed in this way from open collector devices. That is obviously important in the design of commercial logic devices but is not so relevant for the home designer, for whom the extra expense is usually not too great.

We now discuss a group of devices which are extremely important in the design of devices to fit on to the computer databus. They are the three-state buffers, also known as tristate buffers. A three-state device has three output states. I know that one of the first things we learned about logic devices was that there are two logic states, I and 0. Well, the three-state devices have those two states and a third state, known coloquially as the "float" state.

In that condition, the output is said to present a high impedance to any device connected to it. When floating, the output has no effect on a device connected to it. It is as if we had a switch by which we could disconnect the output of the three-state device from the input of the next device. When the output is floating, the three-state device is said to be disabled. When we allow the output to assume a 1 or 0 logic value, we say that the device is enabled.

Control over whether a device is enabled or disabled is done via a pin on the device. Figure seven shows the circuit symbol for a non-inverting Figure 5. Wired AND.

A
B

GA-B

C
D

C
D

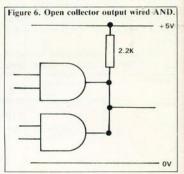
three-state buffer. Let us examine a few practical three-state buffers in the TTL family. The simplest is probably the 74LS125, which is a quad tri-state buffer. The pin-out for the device is shown in figure eight. The circle on the enable line indicates that it is an active low line, i.e., the line is taken to a logic 0 to enable the gates. The 74LS126 is identical except for the small difference that the enable line is taken high to enable the gates.

In each of those devices, there is an individual enable line for each gate in the package. That is not usually the case, as often there are up to eight gates in the package. So the enable lines are said to be commoned, in that several gates are controlled by one enable line. An example is the 74LS244 device which has eight buffers controlled as two blocks of four gates. Figure nine shows an application for this device. When the control signal is low, the data on the eight lines from the external logic is made available for the databus of the computer. The control signal could be generated by the computer. When the signal goes high again, it is as if the LS244 were not connected to the databus of the computer.

All the buffer chips considered

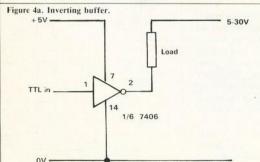
have been uni-directional — in a certain circuit the device has to be rewired to allow a signal to go in the other direction. It would be very useful if devices existed which would allow signals to flow in both directions depending on the state of a control signal to the device.

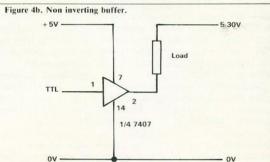
Such a chip exists, the LS245. It is a TTL device and allows two-way or bidirectional data flow depending on the state of one of its pins. The pinout for the device is shown in figure 10. The CE terminal of the chip is the enable line. That is an active low line, as indicated by the bar over the label, and is taken low to enable the buffers. The DIR pin is the pin which

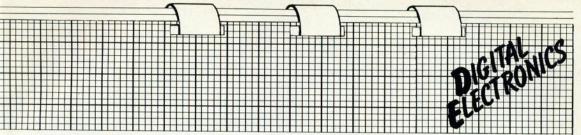


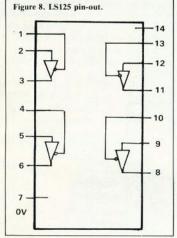
controls the direction of data flow through the buffers.

Table two shows how combinations of those two pins control the buffers. Here, all eight buffers are controlled by the lines simultaneously. Obviously, the device will allow two-way communication between the data lines connected to it. If we enabled the chip and then took DIR to a logic low, data would flow from the B









lines to the A lines. The lines connected to B are said to be transmitting data and the A lines are said to be receiving data. If we took the DIR line high, the situation would be reversed. As eight lines are involved, the device is often described as an octal bus transceiver.

Other three-state devices include the LS367 and the 81LS97. The latter unit is a member of a family of devices designed originally for use with the 8080 microprocessor but is useful in other applications. None of the devices mentioned so far has been latched. The idea will be discussed in detail next time but the basic concepts are that the devices so far examined have, when enabled, re-created at their outputs the data which was at their inputs at that moment.

A latched device can remember data which has been put there by a circuit until it is read. Even after it has been read, the latch can still hold the data until it is told to forget the data.

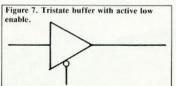
The last operation is called re-setting the latch. Thus the latched buffers are handy devices. In a previous article, in the June-July issue of *Sinclair Projects*, I discussed the 8212 latch in some detail. As we have mentioned the computer bus in passing this time, let us look at it in more detail.

In a computer, the various com-

ponents which make up the device are linked by wires or tracks on printed circuit boards. Some lines, as the wires are called, carry information about the present state of the computer and they are called control signals, as they control interactions between the microprocessor at the heart of the computer and the other devices in the circuit.

Those lines constitute the control bus. Other lines carry information relating to what the computer should do next and on what numbers the computer should next operate. The lines are the databus.

A further set of lines tells the computer where it can find its next instruction or data item. That is the address bus of the computer. The address bus and control bus are



usually uni-directional, although some control lines will be bi-directional. The data bus is bi-directional as it must carry data to and from the microprocessor.

All of those lines carry logic signals and have a fan-out like any other logic device. With regard to the address and control buses, buffering is not usually needed so long as only a few extra LSTTL or CMOS loads are placed on the address lines.

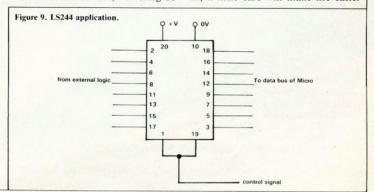
With several devices, buffering be-

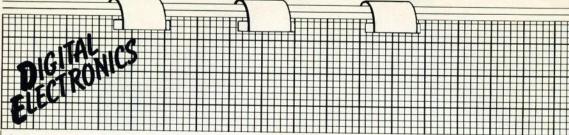
comes necessary and could be done with some of the devices already seen. Buffering between devices added and the databus should be considered to be essential, so that when the device is not in use it does not interfere with the databus in any way. You can imagine the confusion which would confront the microprocessor if two unbuffered devices tried to send signals to it at the same time, the situation which would occur if the devices were not isolated from the bus by the three-state buffers. Circuits which send data to the databus or devices which receive signals from the bus may be isolated by the use of unidirectional three-state buffers but devices which are expected to send and receive information would need bidirectional devices.

Memory chips, such as those which make up the RAM of Sinclair micros, often have three-state buffers built into them so that they can interface directly to the databus.

The concept of the bus will be explored further in a future part of the series. Next time, we will examine the second major group of logic devices — sequential logic circuits. They are the circuits whose logical behaviour depends to a certain extent on signals applied previously.

With regard to practical experiments, this time a few words on using CMOS devices might be in order. Due to their construction, CMOS devices need protecting from static electricity, which can damage them seriously. Although protection is often builtin, a little care will make life easier





for both you and the chip. The devices are usually protected up to about 4,000 volts and a static discharge of below that should not bother them. As you can generally develop a voltage on your fingers of up to 10,000 volts by walking across a nylon carpet, care is still needed. So here are a few points to note when using CMOS devices:

 Never solder to them directly always use sockets. That also makes fault-finding easier.

 Most CMOS devices are delivered to the user with their pins either short-circuited with conductive foam or with the pins stuck into metal foil. Never remove the chip from that protection until you are ready to use it in the circuit.

 When breadboarding circuits, insert the chip last. Never insert or remove a chip from a powered-up circuit.

 Try not to touch the legs of a CMOS device. When handling, try not to wear nylon clothing.

 Before handling, try to discharge any charge on you by touching a cold water tap or pipe. They may seem extreme precautions but they are the ones I employ and I have not lost a CMOS chip yet.

The construction method of the CMOS family makes their inputs very susceptible to electrical noise if they are left unconnected. With TTL de-

vices, unconnected inputs assume a logical value of 1. That is not the case with CMOS gates and it is necessary to tie all unused inputs to either logic 1 or logic 0, depending on the logic function you are trying to achieve in the circuit.

Figure 10. Pin-out of LS245. 20 Vcc DIR 11 19 CE 12 3 18 01 17 5 16 03 15 . 6 15 04 7 16 05 8 13 06 9 12 07 10 11 80

Project buyers' guide

HERE IS a list of suppliers for difficult-to-obtain items which have been used in projects.

Edge connectors 23-way for ZX-81 and 28-way for Spectrum. Innovonics

PCB mounting 3.5mm. jack sockets as used in the Central Heating Controller project.

MS Components Ltd

Extender cards for fitting to rear of edge connector to allow stacking add-ons.

23-way for ZX-81 - ZXTONGUE

28-way for Spectrum — SPECTONGUE Innovonics

MS Components Ltd, Zephyr House, Waring Street, West Norwood, London SE27. Tel: 01-670 4466.

Innovonics, 147 Upland Road, East Dulwich, London SE22.

UPDATE

Errors

December 1983/January 1984

Update, page 14. Waveforms: "lower Q should be \overline{Q} ".

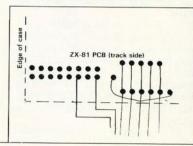
Sound Generator, page 17, figure 3: "CI should be 330pf".

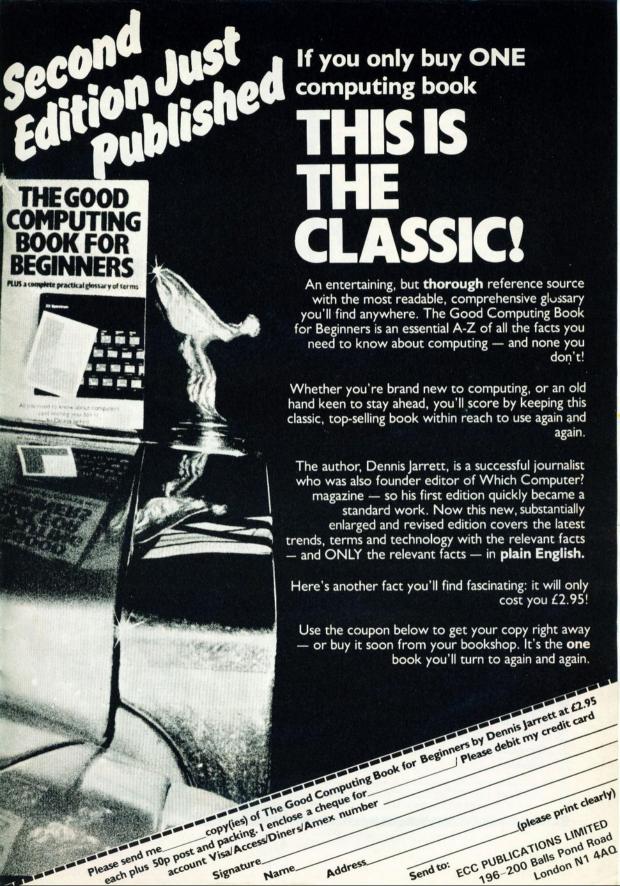
Digital logic, page 24, figure 20: "The NOT gate should be in the other input to AND gate E".

Prowler, page 27, figure 1: "The connection from R4 to IC2 goes to pin I, MR"; page 30, figure 7: "pin 15 is R/W".

Battery support, page 43 circuit layout: "Connection A is by the top end of R3".

Readers' Tips — Four-button keypad, p13. The right-hand side of the figure showing the connections to the ZX-81 PCB should be as below:





Joystick and Interface

for Sinclair Spectrum with these features to give you endless hours of enjoyment.

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The first port simulates 6789 & 0 keys. The second port simulates in [31] command. It will run any Software.

- 1. Using keys 6, 7, 8, 9 and 0. 2. Having redifinable key
- function. Using in (31) i.e. Kempston. Any Software you write
- yourself.

Light pen

The LIGHT PEN enables you to produce high resolution drawings on your own TV screen simply by plugging into the ear socket of your Spectrum. The controlling software supplied with the light pen has 16 pre-defined instructions. You can change colour (Border, Paper, Ink), draw circles, arcs, boxes, lines and insert text onto the screen at any chosen place, you can also draw freehand. There is a feature to retain the screens and animate. On the 48K Spectrum you can retain 5 screens. You can also use the machine code on its own in your own programs, for selecting out of a menu etc. The LIGHT PEN is supplied with a control interface, to adjust the sensitivity/pen alignment.

Keyboard for use with a Spectrum or ZX81

£45.00 Our cased keyboard has 52 keys, 12 of which form a numeric pad. The 12 keys comprise 1-9 numeric plus full stop and shift keys, all in red, to distinguish from the main keyboard keys which are in arey, the keys contrast with grey, the keys contrast with the black case to form a very attractive unit. The case has been designed to take a ZX81 or Spectrum computer. 16K, 32K or 64K can also be fitted to the motherboard inside the case (81 model only). The case is also large enough for other add-ons like the power supply to be fitted, giving a very smart self-contained unit with which other add-ons e.g. printer etc. can still be used. Our ZX Professional keyboard offers more keys and features than

any other model in its price range making it the best value keyboard

available.

diffronics

Spectra-Sound

The so-called speaker in your Spectrum is really on a 'buzzer'. With the DK Tronics "SPECTRA SOUND" you can generate fully amplified sound through the speaker on your TV set. SPECTRA SOUND is a very simple but highly effective add-on. This means that you no longer have a faint beep but a highly amplified sound, which can be adjusted with the TV volume control. The SPECTRA SOUND fits compactly and neatly inside the Spectrum case and is connected by three small crocodile clips.

£9.95

DK Tronics Ltd., Unit 6, Shire Hill Industrial Estate, Saffron Walden, Essex CB11 3AQ. Telephone: (0799) 26350 (24 hrs) 5 lines

Please send me

Please add on £1.25 for post and packing.

Tenclose cheque/PO payable to DK Tronics total E or debit my Access/Barclaycard No.

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